

# Infinite time computations and infinite algorithms

Anton Broberg

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DEPARTMENT OF PHILOSOPHY  
UNIVERSITY OF GOTHENBURG  
SUPERVISOR: FREDRIK ENGSTRÖM



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## ABSTRACT

In this paper we investigate infinite time Turing machines as defined by Hamkins and Lewis in [1]. We extend the result in [2] showing that a larger set of clockable ordinals are 1-tape clockable. Furthermore, a new notion of infinite time Turing machine, in which the set of states may be infinite, is compared with the original notion. We show that the strength of such infinite state infinite time machines correspond to the strength of infinite time Turing machines equipped with real-oracles.

## 1. Introduction

The notion of infinite time Turing machines (defined in section 2) were introduced in [1] by Hamkins and Lewis. Most of the results in section 2,3,5 and 6 are from this article. Some of these results have (more or less) alternative proofs, where the new proof of the Lost Melody Theorem (Theorem 5.11) might be of most interest since the original proof is quite complicated.

Welch shows in [3] that every clockable ordinal is writable (these notions are to be defined in section 3), which was an open question in [1]. The proof is examined in section 4.

In [2], Hamkins and Seabold defines infinite time Turing machines with only one tape (in the original definition the machines have three tapes) and shows that there is clockable ordinals that are not 1-tape-clockable (clockable by a machine with one tape). It is however not known exactly which clockable ordinals that are 1-tape-clockable. This is investigated in section 7 (where the results up until Definition 7.5 are, with some modifications, from [2]).

The main goal of this paper is to investigate what would happen if we were to allow the infinite time Turing machines to have an infinite amount of states. A number of questions arises. How ‘powerful’ will these machines be? If the infinite set of states of such a machine is recursive, will the resulting function this machine computes be ‘ordinary’ infinite time computable? Will these machines be able to solve the halting problems for infinite time machines? These infinite state infinite time machines are defined and examined in section 8.

We will assume some basic knowledge about recursion theory and basic ordinal arithmetics.

Every machine in this paper is fictional and any resemblance with a real machine is coincidental.

## 2. Infinite time Turing machines (IT-machines)

An IT-machine works like a Turing machine that can keep on computing for a transfinite ordinal length of time. The machine has three tapes (an input-tape, a scratch-tape and an output-tape) with countably many cells on each, ordered as  $\omega$ . It has a head that moves along the three tapes, one cell at a time, reading and writing 1's and 0's from/to the cells. The head starts every computation to the far left at the beginning of the tapes.

When the head reads, it reads three cells (one from each tape) at once, but it can only write to one cell at a time. It would make no difference in the computing power (in the sense that the same functions would be computable) if we allowed the machines to read only from one cell at a time, but it would make a difference in the possible lengths of certain computations (more on this in section 7).

A program is a finite set of tuples  $\langle s, r, c, s' \rangle$  that specifies the machines actions, that is, when the machine is in state  $s$  and reads  $r$  from the tapes, it executes command  $c$  and enters state  $s'$ .

The possible commands that the machine can execute is to write 1 to one of the three cells the head is currently on, write 0 to one of the three cells the head is currently on, move the head one step (one cell) to the right on the tapes, move the head one step to the left<sup>1</sup> and finally the machine can halt (that is, end the computation). When the machine executes one of these commands (except for when it halts) we say that the machine has made a step of computation. The length of a computation is the order type of the steps of computation the machine makes before it halts.

After  $\alpha$  steps of computation, where  $\alpha$  is a limit-ordinal the machine enters a limit-stage. Every cell on the tapes that changes value cofinally often before  $\alpha$  is set to 1, and the other cells, that have stabilized, remains the same. The head is instantly placed on the first cells of the tapes and in the program, a special limit-state is consulted.

There are two ways for a machine to halt. Either the machine is told to halt by the program, or there is no tuple in the program that corresponds to the machines current setup.

For convenience, let the set of all possible states be  $\omega$ . Let 1 be the start-state for all programs (so a program that does not contain a tuple with the state 1, is simply the program that halts after 0 steps of computation) and let 0 be the limit-state. The set of possible reads is finite (since there is only  $2^3 = 8$  different setups for the three cells the head is reading) and the set of possible commands is also finite so there is an easy recursive way of coding the set of all possible tuples to  $\omega$ . In this way we can code every program with a finite subset of  $\omega$  (and every finite subset of  $\omega$  codes a program). We fix a recursive bijection between  $\omega$  and the finite subsets of  $\omega$ , thus every  $n \in \omega$  codes a program (and every program is coded by an  $n \in \omega$ ). This allows us to be a little careless and call a natural number  $p$  a program when we really mean the program that  $p$  codes.

The machine can be told to do different things at once, for example: both  $\langle s, r, c_0, s'_0 \rangle$  and  $\langle s, r, c_1, s'_1 \rangle$  may be elements of a program, but then the machine chooses to obey the tuple with the least natural number coding it.

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<sup>1</sup>If a machine is told by the program to move the head a step to the left when the head is placed at the beginning of the tapes, the machine simply ignores the command.

At the start of the computation the input is written to the input-tape, and the scratch-tape and the output-tape are filled with 0's.

The inputs and outputs for the machines are countable binary strings. These binary strings will be referred to as reals.

Every program  $p$  determines a partial function  $\varphi_p: 2^\omega \rightarrow 2^\omega$  simply by letting the value of  $\varphi_p(x)$  be what is on the output-tape when the machine with program  $p$  and input  $x$  halts, and letting  $\varphi_p(x)$  be undefined (which will be denoted  $\varphi_p(x)\uparrow$ ) when the machine does not halt. Of course  $\varphi_p = \varphi_q$  iff  $\varphi_p(x)\downarrow \leftrightarrow \varphi_q(x)\downarrow$  for all  $x$  and  $\varphi_p(x) = \varphi_q(x)$  for all  $x$  such that  $\varphi_p(x)\downarrow$ .

By adding extra input-tapes we get functions of many variables as well.

A (partial) function  $f$  on reals is IT-computable if there is a program  $p \in \omega$  such that  $\varphi_p = f$  (that is  $\varphi_p(x) = f(x)$  if  $f(x)$  defined and  $\varphi_p(x)\uparrow$  otherwise).

A set of reals  $A$  is IT-decidable if its characteristic function is IT-computable, and  $A$  is IT-semi-decidable if there is an IT-computable function  $f$  such that  $f(x) = 1$  iff  $x \in A$ .

We will regard the natural numbers as a subset of the reals by letting the real where the first  $n$  digits are 1 and the rest is 0, represent the natural number  $n$ .

From now on, 'decidable', 'computable' and so forth will mean IT-decidable and IT-computable while 'recursive' will refer to ordinary recursiveness in the Turing sense.

**THEOREM 2.1:** For every computable function  $\varphi_p$  such that  $\varphi_p$  is defined exactly on  $\omega$ ,  $\varphi_p(n)$  is not computable in less than  $\omega$  steps of computation for any  $n \in \omega$ .

*Proof:* Assume the machine with program  $p$  and input  $n \in \omega$  halts in  $k$  steps of computation for some  $k \in \omega$ . In  $k$  steps of computation, the head has not time to move more than at most  $k$  steps to the right on the tapes. Thus, if we run this program with an input  $x$  that is any real where the first  $k + 1$  digits in  $x$  are like the first  $k + 1$  digits in  $n$ , this program will halt in  $k$  steps on this input as well. But this is a contradiction since  $\varphi_p$  is defined only on  $\omega$ .  $\dashv$

**REMARK 2.2:** If the tapes look exactly the same in two limit-stages and none of the cells that are 0 in these limit-stages, turns 1 at any point between them, then the machine is caught in a loop. Since the head always is in the same place in limit-stages and the program always consults the limit-state in limit-stages, the exact same thing will happen again. But if any of the cells that are 0 turns 1 at some point between the two limit-stages, then the machine could escape the loop in a later limit-of-limits where this cell then turns 1 (for an example of such an escape, see (6) on page 6).

**THEOREM 2.3:** Every halting IT-computation is countable.

*Proof:* Suppose a computation has not halted after  $\omega_1$  steps of computation. Let  $\alpha_0$  be the first stage where all the cells that locally stabilize before  $\omega_1$  has already stabilized. Since there are only countably many such cells, we have that  $\alpha_0 < \omega_1$  ( $\alpha_0$  is the supremum of a countable set of countable ordinals and therefor countable). The cells that change value at all between  $\alpha_0$  and  $\omega_1$  will change value cofinally often between  $\alpha_0$  and  $\omega_1$ , so there must be a countable sequence  $\alpha_0 < \alpha_1 < \alpha_2 < \dots$

of countable ordinals such that all the cells that change at all after  $\alpha_n$  have changed value at least once before  $\alpha_{n+1}$ . But then stage  $\delta = \sup\{\alpha_n\}$  is a countable limit-stage where the tapes look exactly like they do at stage  $\omega_1$  and none of the cells that are 0 at stage  $\delta$  will turn 1 again before  $\omega_1$ . This computation is therefore caught in a loop (according to Remark 2.2) and will never halt.  $\dashv$

Since we in this proof can choose  $\alpha_0$  as big as we want, as long as it is countable, we can choose  $\alpha_0$  to be bigger than  $\delta$  and we would produce another  $\delta' > \delta$  that is a countable limit-stage where the tapes look exactly like in stages  $\delta$  and  $\omega_1$ . Hence, we can draw the following conclusion.

**COROLLARY 2.4:** Every non-halting computation is caught in a loop, and the length of this loop is countable.

**THEOREM 2.5:** Every recursively enumerable set  $A \subseteq \omega$  is decidable.

*Proof:* We specify the program  $p$  that decides  $A$ . The program is set up so that if there is a 1 on the first cell of the scratch-tape in a limit-stage, the machine halts with output ‘no’. On input  $x$ ,  $p$  first decides if  $x$  is a natural number or not.  $p$  does this by first writing a 1 to the first cell of the scratch-tape and then it starts searching for the first 0 on the input-tape. If there is no first 0, that is, if the input-tape is filled with 1’s, the search will be in vain and the machine will, after  $\omega$  steps of computation, be in a limit-stage with a 1 on the first cell of the scratch-tape, and the machine will halt with output ‘no’. If it finds a 0 it moves back to the first cell and writes a 0 to the scratch-tape. Then it returns to the cell where it read the first 0 and starts a search for the next 1. If another 1 is found,  $x$  is not a natural number and we halt with output ‘no’, but if there is no 1 (after the first 0), the machine will, after  $\omega$  steps of computation, be in a limit-stage with a 0 on the first cell of the scratch-tape. And it is easy to see that this happens exactly when  $x$  is a natural number.

Thus, if the machine has a 0 on the scratch-tape in the first limit-stage, this means that the input  $x$  is a natural number, and it is time for the machine to decide if  $x \in A$  or not. Since  $A$  is recursively enumerable, there is a Turing machine that halts iff the input is a member of  $A$ . So now the machine first writes a 1 to the first cell of the scratch-tape and then simply simulates this Turing machine on the input-tape where the input  $x$  still is untouched. If the simulated machine halts, then  $p$  halts, since this implies that  $x \in A$ . If the simulated machine does not halt, the machine will, after another  $\omega$  steps of computation, be in a limit-stage with a 1 on the scratch-tape and consequently, the machine halts with output ‘no’.  $\dashv$

**COROLLARY 2.6:** The halting problem for Turing machines is solvable by an IT-machine.

*Proof:* Follows directly since  $K = \{n; \text{the } n\text{'th Turing machine halts on input } n\}$  is recursively enumerable.  $\dashv$



Every relation  $\triangleleft \subseteq \omega \times \omega$  is coded by some real  $x$  by letting the  $\langle n, m \rangle$ 'th digit in  $x$  be 1 iff  $n \triangleleft m$  (where  $\langle \cdot, \cdot \rangle$  is some computable pairing function). It is easy to see that every real codes a binary relation in this way and every binary relation is coded by a real.

Let WO be the set of reals that codes well orders and for all  $x \in \text{WO}$  let  $\|x\|$  be the order type of the well order that  $x$  codes, hence if  $x \in \text{WO}$ , then  $\|x\|$  is a countable ordinal.

WO is  $\Pi_1^1$ -complete, which means that WO is  $\Pi_1^1$  and if  $A$  is  $\Pi_1^1$ , then there is a recursive function  $f$  on the reals such that  $x \in A$  iff  $f(x) \in \text{WO}$ .<sup>2</sup>

In [1], Hamkins and Lewis shows that WO is IT-decidable and since WO is  $\Pi_1^1$ -complete, this implies that all  $\Pi_1^1$ - and subsequently also all  $\Sigma_1^1$ -sets are decidable. It is also shown that the power of the IT-machines extends further by showing that some  $\Delta_2^1$ -sets (that are not  $\Pi_1^1$ - or  $\Sigma_1^1$ -sets) are decidable. But this is where it stops. All decidable and semi-decidable sets are  $\Delta_2^1$  (for details on this, see [1]).

**THEOREM 2.7 (S-N-M):** There is a recursive function  $s$  such that  $\varphi_p(\bar{k}, \bar{x}) = \varphi_{s(p, \bar{k})}(\bar{x})$ , for all reals  $\bar{x}$  and natural numbers  $p$  and  $\bar{k}$ .

The classical proof of the s-n-m theorem works in this context as well.

Notice that  $s$  is defined only on  $\omega$  and hence the  $k_i$ 's in  $\bar{k}$  must not be any reals but natural numbers. If we let go of this restriction we get in a lot of trouble. Assume there is a computable function  $s$  such that  $\varphi_p(\bar{k}, \bar{x}) = \varphi_{s(p, \bar{k})}(\bar{x})$ , for all reals  $\bar{x}$  and  $\bar{k}$  and natural numbers  $p$ , and consider the computable function  $\varphi_p$  such that  $\varphi_p(x, y) = x$  for all reals  $x$  and  $y$ . Then we have that  $\varphi_{s(p, x)}(y) = x$ . But then  $\varphi_{s(p, x)}(0) = x$ . This means that for any reals  $x$  and  $x'$  such that  $x \neq x'$ , we have that  $\varphi_{s(p, x)} \neq \varphi_{s(p, x')}$  which would imply that there is at least  $2^{\aleph_0}$  many computable functions and that is a contradiction.

**THEOREM 2.8 (RECURSION):** For any IT-computable total  $f: \omega \rightarrow \omega$ , there is a program  $p$  such that  $\varphi_p = \varphi_{f(p)}$ .

Again we omit the proof since the classical proof goes through.

### 3. Clockable and writable ordinals

**DEFINITION 3.1:** An ordinal  $\alpha$  is *clockable* if there is an IT-machine that halts after  $\alpha$  steps of computation on input 0. Let CIO be the set of clockable ordinals.

**DEFINITION 3.2A:** A real  $x$  is *writable* if there is an IT-machine that halts with output  $x$  on input 0. Let WrR be the set of writable reals.

**DEFINITION 3.2B:** An ordinal  $\alpha$  is *writable* if there is a real  $x \in \text{WrR} \cap \text{WO}$  such that  $\|x\| = \alpha$ . Let WrO be the set of writable ordinals.

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<sup>2</sup> $A$  is  $\Pi_1^1$  if there is a second-order arithmetical formula  $\forall \bar{X} \psi(\bar{X}, Y)$ , where  $\psi$  is a first-order formula, that defines  $A$  in the standard model.

So which ordinals are clockable? Let us begin by recognizing some simple clockable ordinals:

- (1)  $n \in \text{ClO}$  for all  $n \in \omega$ . (Consider a program with  $n$  states.)
- (2)  $\omega \in \text{ClO}$ . (Let the limit-state halt the machine.)
- (3)  $\alpha \in \text{ClO} \Rightarrow \alpha + 1 \in \text{ClO}$ . (Just add an extra state to the program and instead of halting, go one step to the right and enter this new state and halt.)
- (4)  $\alpha \in \text{ClO} \Rightarrow \alpha + \omega \in \text{ClO}$ . (Move the computation one cell to the right and leave the first cell untouched. In the limit-state check if the first cell is 1, if so halt, otherwise continue with the computation. Instead of halting when the original machine halts, move to the first cell and write 1. Then enter a new state that keeps the head stepping to the right. In the next limit-stage, when the program consults the limit-state and reads a 1 from the tape the machine will halt. This will take  $\alpha + n + \omega = \alpha + \omega$  steps.)
- (5)  $\alpha \in \text{ClO} \Rightarrow \alpha + \beta \in \text{ClO}$  for all  $\beta < \omega^2$ . (Follows from (3) and (4) since every  $\beta < \omega^2$  can be written on the form  $\omega \cdot n + k$  for some natural numbers  $n$  and  $k$  (Cantor's normal form).)
- (6)  $\omega^2 \in \text{ClO}$ . (Consider the machine that only steps to the right and in every limit first writes 1 to the first cell and then writes it over with a 0 (this will be referred to as 'flashing a flag'). Since  $\omega^2$  is the first limit of limits, this will be the first time the first cell is 1 in a limit-stage. This will be noticed by the program and the machine will halt.)

**THEOREM 3.3:** Every recursive ordinal is clockable.

*Proof:* Cantor's normal form implies that every ordinal can be written on the form  $\alpha + \beta$  where  $\alpha$  is a limit of limits (or 0) and  $\beta < \omega^2$ , hence (5) above implies that it suffices to show that every recursive limit of limits ordinal is clockable. The proof of this is described in great detail in [1] and will only be sketched here. Let  $\alpha$  be a recursive limit of limits ordinal. Then there is a real  $x$  such that  $x$  is writable in  $\omega$  many steps and  $\|x\| = \alpha$ . So we construct a machine that first writes  $x$  and then erases the smallest element in the order-relation that  $x$  codes until the relation is empty, then it stops. This machine halts after  $\omega + (\omega + \omega)\alpha + \omega$  steps (first  $\omega$  steps to write  $x$ , then for every element in the relation that  $x$  codes, it will take  $\omega$  steps to find the smallest element and  $\omega$  steps to erase all mention of this element, then it will take  $\omega$  steps to realize that the relation is empty). Then we use that  $\alpha$  is a limit of limits to shorten this algorithm to the extent that it actually will be done in  $\alpha$  many steps (see [1] for details). Hence  $\alpha \in \text{ClO}$ .  $\dashv$

**THEOREM 3.4 (SPEED-UP LEMMA):** If  $\alpha + n \in \text{ClO}$ , then  $\alpha \in \text{ClO}$  for all  $\alpha$  and all  $n \in \omega$ .

*Proof:* We may assume that  $\alpha$  is a limit ordinal, since it is easy to make a computation a finite amount of steps longer. Consider the program  $p$  that halts after  $\alpha + n$  steps of computation. In the limit-stage  $\alpha$  the first  $n + 1$  cells on the tapes must be set up in such a way that the machine will halt  $n$  steps from there. Denote these  $n + 1$  cells with the tuple  $\bar{a}$ . Now consider the program that simulates  $p$  but moves the computation one step to the right, leaving the first cell on every tape

untouched. This program halts after  $\alpha + n + 1$  steps of computation, and  $\alpha$  is the first limit-stage where cells 2 to  $n + 2$  on each tape are set up like  $\bar{a}$ . But we can see this already in the limit by flashing a flag every time any cell that is 0 in  $\bar{a}$  is not 0, and by flashing a second flag every time every cell that is 1 in  $\bar{a}$  has been 1 at some point since the last time we flashed the second flag. Then the first flag is 0 and the second flag is 1 in a limit-stage if and only if cells 2 to  $n + 2$  on the tapes are set up as  $\bar{a}$ , and since the head can see three cells at once, and the first cell on every tape is free to use as flags, it is no problem to see the two flags in the limit and halt. Hence  $\alpha \in \text{ClO}$ .  $\dashv$

This proof uses the fact that the machines can read from more than one cell at a time, and in fact the Speed-up Lemma does not hold if we only allow the machines to read from one tape at a time (see section 7).

Many of the proofs ahead will use some variation of a universal machine that simulates all computations on the form  $\varphi_p(0)$ . By slicing up one of the tapes in  $\omega$  parts and then slice up every one of those slices in three, a universal machine can actually do this on one single tape or even a slice of a tape (one have to do some modifications in what constitutes a step of computation in the simulated computations (see Remark 7.10)). Sometimes we only need the universal machine to simulate, in  $\omega$  many steps, one step of computation in every simulated computation but often we need the universal machine to simulate, in  $\omega$  many steps,  $\omega$  steps of computation in every simulated computation. This can be done by simulating things in a certain order, for example, consider the recursive bijection from  $\omega$  to  $\omega \times \omega$  that orders  $\omega \times \omega$  like this:  $\langle 0, 0 \rangle, \langle 0, 1 \rangle, \langle 1, 0 \rangle, \langle 0, 2 \rangle, \langle 1, 1 \rangle, \langle 2, 0 \rangle, \langle 0, 3 \rangle, \langle 1, 2 \rangle, \langle 2, 1 \rangle, \langle 3, 0 \rangle, \dots$  where  $\langle p, k \rangle$  represents the  $k$ 'th step in the computation of program  $p$  on input 0.

**THEOREM 3.5:** There are gaps in the clockable ordinals, and the first gap after any clockable ordinal has size  $\omega$ .

*Proof:* Let  $\alpha \in \text{ClO}$  and let  $\beta$  be the first non-clockable ordinal greater than  $\alpha$ . It is easy to see that  $\beta$  must be a limit-ordinal, and the Speed-up Lemma implies that  $\beta + n \notin \text{ClO}$  for all  $n \in \omega$ . We show that  $\beta + \omega \in \text{ClO}$ . Consider a universal machine that on input 0 simulates all computations on the form  $\varphi_p(0)$  and makes  $\omega$  steps of computation of every simulated computation in  $\omega$  steps. It keeps track on one of the computations that halts in  $\alpha$  many steps, and when this simulated computation halts, it starts to flash a flag every time a simulated computation halts. Then it simply halts in the first limit-stage where this flag is 0. Since  $\beta$  is the first non-clockable ordinal greater than  $\alpha$ , this flag will be 1 in every limit-stage between  $\alpha$  and  $\beta$ . And it will be 1 in stage  $\beta$  too since the flag will be flashed cofinally often before  $\beta$ . But for no  $n \in \omega$  will the flag be flashed at stage  $\beta + n$  so in stage  $\beta + \omega$  will this flag be 0. Hence, this universal machine halts in  $\beta + \omega$  many steps of computation so  $\beta + \omega \in \text{ClO}$  which proves the theorem.  $\dashv$

It is shown in [1] that the first non-clockable ordinal is the supremum of the recursive ordinals  $\omega_1^{CK}$ , and in fact, for every  $A \subseteq \omega$  we have that the supremum of the  $A$ -recursive ordinals is not clockable.<sup>3</sup>

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<sup>3</sup>This follows from a theorem in [1] stating that no admissible ordinal is clockable.

**THEOREM 3.6:** For every  $\alpha \in \text{ClO}$ , there is a gap of size at least  $\alpha$  in the clockable ordinals.

*Proof:* To prove this theorem we will specify a universal machine that would halt after the supremum of the clockable ordinals if no gap of size at least  $\alpha$  existed. The Speed-up Lemma implies that all gaps are of limit ordinal length so we can assume that  $\alpha$  is a limit ordinal  $\geq \omega \cdot 2$ . Simply simulate all computations on the form  $\varphi_p(0)$ . In every limit, start clocking  $\alpha$  until a simulated computation halts. Halt when the computation that clocks  $\alpha$  halts. This occurs the first time it has been  $\alpha$  steps since the last simulated computation halted. So if there are no gaps of size  $\alpha$ , this computation will halt after the supremum of the clockable ordinals. (Some details are omitted here. For example would we need a flag so the machine knows if it should start over the  $\alpha$ -clock in the limit-stages or not. There are also some technical problems with the  $\alpha$ -clock that are addressed in Remark 7.7)  $\dashv$

Since the first gap after any clockable ordinal is of size  $\omega$ , the gaps of size greater than  $\omega$  must start with an ordinal that is a limit of ordinals that starts gaps of size  $\omega$ .

**THEOREM 3.7:** If  $\alpha \in \text{ClO} \cup \text{WrO}$ , the order type of the gaps of size at least  $\alpha$  is neither clockable nor writable.

The proof of Theorem 3.7 is basically a generalization of the proof of Theorem 3.6 and is left out.

**DEFINITION 3.8:** A real is *eventually writable* if it is written to the output-tape of a (halting or non-halting) computation on input 0, and never is overwritten or altered. Let  $\text{EWrR}$  be the set of eventually writable reals.

An ordinal  $\alpha$  is eventually writable if  $\alpha \in \text{EWrO} = \{\|x\|; x \in \text{WO} \cap \text{EWrR}\}$

**DEFINITION 3.9:** A real is *accidentally writable* if it at some point of a (halting or non-halting) computation on input 0 appears on one of the tapes. Let  $\text{AWrR}$  be the set of accidentally writable reals.

An ordinal  $\alpha$  is accidentally writable if  $\alpha \in \text{AWrO} = \{\|x\|; x \in \text{WO} \cap \text{AWrR}\}$

Let  $\lambda = \sup \text{WrO}$ ,  $\zeta = \sup \text{EWrO}$  and  $\Sigma = \sup \text{AWrO}$ . It follows practically directly from the definitions that  $\text{WrR} \subseteq \text{EWrR} \subseteq \text{AWrR}$ , and subsequently also that  $\text{WrO} \subseteq \text{EWrO} \subseteq \text{AWrO}$ , and subsequently  $\lambda \leq \zeta \leq \Sigma$ . We shall see later that in fact  $\lambda < \zeta < \Sigma$ , and hence all the inclusions above are strict.

**THEOREM 3.10:** There are no gaps in the writable ordinals.

*Proof:* Assume  $\alpha \in \text{WrO}$ . We show that an arbitrary  $\beta < \alpha$  also is writable. Since there is a real  $x \in \text{WrR}$  such that  $x$  codes a well order  $\triangleleft$  and  $\|x\| = \alpha$ , there must be an  $n \in \omega$  such that if a real  $y$  codes the relation we get if we erase all mentioning of any  $m$  such that  $n \triangleleft m$  out of the relation that  $x$  codes, then  $\|y\| = \beta$ . So since  $y$  is writable (just write  $x$  and start erasing), we have that  $\beta \in \text{WrO}$ .  $\dashv$

**THEOREM 3.11:** The order types of  $\text{ClO}$  and  $\text{WrO}$  are both equal to  $\lambda$ .

*Proof:* The order type of WrO is  $\lambda$  since there are no gaps in WrO. First we show that the order type of ClO is greater than or equal to  $\lambda$  by showing that for every  $\alpha \in \text{WrO}$ , there are at least  $\alpha$  many<sup>4</sup> clockable ordinals.

Fix an  $x \in \text{WrR}$  such that  $\|x\| = \alpha$ . For every  $n \in \omega$ , consider the programs  $p_n$  that writes up  $x$  on a tape and then starts erasing the  $\triangleleft$ -least element in the relation that  $x$  codes until  $n$  is the  $\triangleleft$ -least element in the relation. For every  $n \in \omega$ , let  $\alpha_n$  be the ordinal clocked by  $p_n$ . Then for every  $n, m \in \omega$  such that  $n < m$ , we have that  $\alpha_n < \alpha_m$ . Hence, the order type of the clockable ordinals generated by these programs is  $\alpha$ .

Now for the other direction we show that the order type of all clockable ordinals less than an arbitrary  $\alpha \in \text{ClO}$ , is writable. Consider a universal machine that simulates all the computations on the form  $\varphi_p(0)$  and at the same time clocks  $\alpha$ , in such a way that one step of computation is made in every  $\varphi_p(0)$  and in the  $\alpha$ -clock, in  $\omega$  many steps. We will make sure that this machine writes a real  $x$ , that codes a relation  $\triangleleft$  such that  $\|x\|$  is of the wanted order type, at the output-tape. At the start and after every limit-stage, it first makes one step in the  $\alpha$ -clock and then it makes one step in  $\varphi_0(0), \varphi_1(0), \varphi_2(0)\dots$  and so forth, and as soon as a computation  $p$  halts, it will add  $p$  to the relation  $\triangleleft$  by writing a 1 to the  $\langle q, p \rangle$ 'th cell on the output-tape for every  $q$  that has been added earlier. And then nothing more is done to the output-tape until the next limit-stage is reached, regardless if more simulated computations halt. When the  $\alpha$ -clock halts, we halt with a real  $x$  written to the output-tape that codes a relation  $\triangleleft$  such that  $p \triangleleft q$  if the program  $p$  halts before  $q$  on input 0, and both halts before  $\alpha$ . Since only the 'least' program that halts in a certain amount of steps are members of the relation,  $x$  will actually code a well order, and in fact  $\|x\|$  is equal to the order type  $\beta$  of all clockable ordinals smaller than  $\alpha$ , and consequently  $\beta \in \text{WrO}$ .  $\dashv$

**THEOREM 3.12:**  $\lambda \notin \text{ClO} \cup \text{WrO}$  but  $\lambda \in \text{EWrO}$ .

*Proof:* To see that  $\lambda \in \text{EWrO}$ , just run the same algorithm as in the second part of the proof of Theorem 3.11, but without the  $\alpha$ -clock. That program would run forever and eventually write  $\lambda$  to the output-tape.

Of course  $\lambda \notin \text{WrO}$ , but to see that  $\lambda \notin \text{ClO}$ , assume that  $\lambda \in \text{ClO}$  and simulate all computations on the form  $\varphi_p(0)$  and every time a computation halts, make one step of the computation that clocks  $\lambda$ . When this  $\lambda$ -clock halts, we halt. But since the order type of ClO is  $\lambda$ , this computation will halt after the supremum of the clockable ordinals. That is a contradiction.  $\dashv$

**THEOREM 3.13:**  $\lambda < \zeta < \Sigma$ .

*Proof:* That  $\lambda < \zeta$  follows directly from 3.12. To see that  $\zeta < \Sigma$ , consider the universal machine that simulates all computations on the form  $\varphi_p(0)$  on the scratch-tape, and after it has made one step of computation on every simulated computation it adds up all the ordinals that are written to the simulated output-tapes (to see

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<sup>4</sup>What we really mean when we write that 'there are  $\alpha$  many clockable ordinals', is that the order type of these ordinals is  $\alpha$ . We will misuse the word 'many' in this way throughout this paper.

that this is a computable procedure, see Remark 3.14) and writes the resulting ordinal to the real output-tape. Eventually, all the eventually writable ordinals will be written to the output-tapes of the simulated computations, and after that point, the ordinals written to the real output-tape will always be greater or equal to  $\zeta$ . Since these ordinals are written to a tape, they are accidentally writable and consequently strictly smaller than  $\Sigma$ . Hence  $\zeta < \Sigma$ .  $\dashv$

REMARK 3.14: It is no problem to check if a real on a simulated output-tape codes an ordinal or not so assume this is done and we have  $\omega$  ordinals on slices of the scratch-tape. Let  $z_0, z_1, \dots$  be the reals coding these ordinals. We now define a relation  $\triangleleft$  on  $\omega$  such that  $\langle n, k \rangle \triangleleft \langle n', k' \rangle$  iff  $k$  is related to something in the well-order  $z_n$  codes and  $k'$  is related to something in the well-order  $z_{n'}$  codes and either  $n < n'$  or  $n = n'$  and  $\langle k, k' \rangle$  is a member of the well-order that  $z_n$  codes. This relation is a well-order and the order type is equal to  $\|z_0\| + \|z_1\| + \dots$ , and this relation is decidable (with access to  $z_0, z_1, \dots$ ) so the real coding it is writable (with access to  $z_0, z_1, \dots$ ) and this real is what we write to the output-tape in Theorem 3.13.

#### 4. CIO $\subseteq$ WrO

An open question in Hamkins and Lewis's article ([1]) was whether CIO  $\subseteq$  WrO or not. This was answered affirmative by Philip Welch in [3].

Assume that we have ordered the cells on the three tapes as  $\omega$  in some computable way.

DEFINITION 4.1: For every  $i, p \in \omega$  and every ordinal  $\alpha$ ,  $C_i^p(\alpha)$  is the value (0 or 1) of the  $i$ 'th cell after  $\alpha$  steps of computation with program  $p$  on input 0.

DEFINITION 4.2:  $\delta_i^p(\alpha) = \sup \{ \beta < \alpha; \beta = 0 \vee C_i^p(\beta) \neq C_i^p(\beta + 1) \}$ , for every  $i, p \in \omega$  and every ordinal  $\alpha$ .

Hence  $\delta_i^p(\alpha)$  is the last stage, before  $\alpha$ , that the  $i$ 'th cell changed value in the computation of  $\varphi_p(0)$ . It is easy to see that when  $\alpha$  is a limit ordinal, we have that  $\delta_i^p(\alpha) = \alpha$  iff the  $i$ 'th cell changed value cofinally often before  $\alpha$  (or equivalently  $\delta_i^p(\alpha) \neq \alpha$  iff  $\delta_i^p(\alpha) < \alpha$  iff the  $i$ 'th cell has stabilized before  $\alpha$ ). Of course, if  $\delta_i^p(\alpha) < \alpha$  and the  $i$ 'th cell has stabilized before  $\alpha$ , it might still 'destabilize' at some stage after  $\alpha$ .

THEOREM 4.3:  $\delta_i^q(\Sigma) < \Sigma \Rightarrow \delta_i^q(\Sigma) < \zeta$ , for every  $i, q \in \omega$ .

*Proof:* Theorem 4.3 is proved in [3] and will not be rigorously proved here. In words it states that if a cell  $i$  in a computation (on input 0) has (locally) stabilized before  $\Sigma$ , it actually stabilized already before  $\zeta$ . Welch proves this by constructing a non-halting universal machine  $M(q, i)$  that, if  $\delta_i^q(\Sigma) < \Sigma$ , eventually writes  $\delta_i^q(\Sigma)$  to the output-tape, and never writes it over. Hence, the conclusion that  $\delta_i^q(\Sigma) \in \text{EWro}$ , can be drawn, which proves the theorem.

The way that  $M(q, i)$  does this is that after every time it has made one step of computation on every simulated computation on the form  $\varphi_p(0)$ , it computes the sum  $\sigma$  of all the ordinals that are written to any of the simulated computations

simulated tapes (this can be done according to Remark 3.14), then it computes  $\delta_i^q(\sigma)$ . Let  $x$  be what is currently on the output-tape at this point. Now the machine checks if  $x \in \text{WO}$ , if  $\delta_i^q(\sigma) \leq \|x\|$  and if  $\delta_i^q(\|x\|) = \|x\|$ . If  $x$  withstands all these tests, the output-tape is left as it is, otherwise (a code for)  $\delta_i^q(\sigma)$  is written to the output-tape instead of  $x$ .

Now assume that  $\delta_i^q(\Sigma) < \Sigma$ . We show that  $\delta_i^q(\Sigma) < \zeta$ . First we show that  $\delta_i^q(\Sigma)$  will be written to the output-tape of  $M(q, i)$  at some point of the computation. At some point will an accidentally writable ordinal that is greater than  $\delta_i^q(\Sigma)$  be written to a simulated tape, so the sum  $\sigma$  will at this point be such that  $\delta_i^q(\Sigma) < \sigma < \Sigma$ , and consequently will  $\delta_i^q(\Sigma) = \delta_i^q(\sigma)$ . The only way that (a code for)  $\delta_i^q(\sigma) = \delta_i^q(\Sigma)$  is not written to the output-tape at this point is if  $x \in \text{WO}$ ,  $\delta_i^q(\sigma) = \delta_i^q(\Sigma) \leq \|x\|$  and  $\delta_i^q(\|x\|) = \|x\|$ , where  $x$  is what is currently on the output-tape. But if  $x$  withstands these tests we must have that  $\delta_i^q(\Sigma) = \|x\|$  since  $\delta_i^q(\|x\|) = \|x\|$  and  $\delta_i^q(\Sigma) \leq \|x\|$ . In either case must  $\delta_i^q(\Sigma)$  at some point be written to the output-tape.

It is fairly easy to check that when (a code for)  $\delta_i^q(\Sigma)$  has been written to the output-tape, it will never be overwritten, by basically the same arguments as above, hence  $\delta_i^q(\Sigma) \in \text{EWro}$ , which proves the theorem.  $\dashv$

STATEMENT 4.4:  $\delta_i^q(\Sigma) = \Sigma \Rightarrow \delta_i^q(\zeta) = \zeta$ , for every  $q, i \in \omega$ .

Some version of Statement 4.4 is used in [3] to prove the statements (I) and (II) below, but is never stated nor proved in [3].

For every program  $p \in \omega$ , we have:

(I)  $C_i^p(\Sigma) = C_i^p(\zeta)$  for all  $i \in \omega$ .

(II)  $C_i^p(\zeta) = 0$  implies that  $C_i^p(\xi) = 0$  for all  $\zeta < \xi < \Sigma$ .

If  $\delta_i^p(\Sigma) < \Sigma$ , Theorem 4.3 shows that  $\delta_i^p(\Sigma) < \zeta$  and (I) follows. On the other hand if  $\delta_i^p(\Sigma) = \Sigma$ , Statement 4.4 shows that  $\delta_i^p(\zeta) = \zeta$  and hence  $C_i^p(\Sigma) = C_i^p(\zeta) = 1$ . Either way, (I) holds.

$C_i^p(\zeta) = 0$  implies that  $\delta_i^p(\zeta) < \zeta$  which implies (according to Statement 4.4) that  $\delta_i^p(\Sigma) < \Sigma$  which implies (according to Theorem 4.3) that  $\delta_i^p(\Sigma) < \zeta$  which implies that  $C_i^p(\xi) = 0$  for all  $\zeta < \xi < \Sigma$ , hence, (II) holds as well.

(I) and (II) implies that if  $\varphi_p(0)$  has not halted before  $\Sigma$ , it is caught in a loop and will never halt (according to Remark 2.2), in other words, every halting computation (on input 0) halts before  $\Sigma$ .<sup>5</sup>

THEOREM 4.5:  $\mu < \lambda$  for every  $\mu \in \text{ClO}$ , and consequently, since there are no gaps in the writable ordinals,  $\text{ClO} \subseteq \text{WrO}$ .

*Proof:* Let  $p$  be a program such that  $\varphi_p(0)$  halts after  $\mu$  steps of computation. Then we know that  $\mu < \Sigma$ . Let  $q$  be a program such that  $\varphi_q(0)$  at some point writes  $\mu' \geq \mu$  on some of its tapes. Let  $N$  be a machine that simulates  $\varphi_q(0)$  on

<sup>5</sup>In [3], Welch draws the conclusion that all computations on input 0 halts before  $\zeta$  from (I) and (II). Unfortunately, we fail to see how this conclusion can be drawn, but it does not really matter since we (with some minor modifications) only need them to halt before  $\Sigma$ .

a slice of the scratch-tape, and every time an ordinal  $\alpha$  is written to one of  $\varphi_q$ 's simulated tapes,  $N$  writes  $\alpha$  on the real output-tape and then runs  $\alpha$  steps in the computation of  $\varphi_p(0)$ . If  $\varphi_p(0)$  halts, then  $N$  halts. Eventually  $\varphi_q(0)$  will write an ordinal  $\bar{\mu} \geq \mu$  on the output-tape and then  $\varphi_p$  will have time to halt so  $N$  will halt with  $\bar{\mu}$  on the output-tape, so  $\bar{\mu} \in \text{WrO}$ . Hence  $\mu \leq \bar{\mu} < \lambda$ , which proves the theorem.  $\dashv$

**COROLLARY 4.6:**  $\sup \text{ClO} = \lambda$ .

*Proof:* For every  $\alpha \in \text{WrO}$  there is a  $\beta \in \text{ClO}$  such that  $\alpha \leq \beta$ . For example,  $\beta$  could be the ordinal clocked by the program that first writes  $x$  such that  $\|x\| = \alpha$  to a tape and then starts erasing the smallest element from the relation that  $x$  codes and halts when this relation is empty. This computation will take at least  $\alpha$  many steps before it halts. Hence,  $\sup \text{ClO} \geq \lambda$ . But Theorem 4.5 implies that  $\sup \text{ClO} \leq \lambda$ , hence  $\sup \text{ClO} = \lambda$ .  $\dashv$

Although we have no proof for Statement 4.4, we will for the rest of this paper assume that it is true, and hence that in fact  $\sup \text{ClO} = \lambda$ .

## 5. The halting problems

The halting problem can be relativized to IT-machines in two different ways,  $H = \{(p, x) : \varphi_p(x) \downarrow\}$  and  $h = \{p : \varphi_p(0) \downarrow\}$  which are not (as their classical analogs) equivalent. Notice also that  $h \subseteq \omega$ , but  $H \not\subseteq \omega$ .

It is easy to see that  $H$  and  $h$  are semi-decidable, just simulate  $\varphi_p(x)$  and if it halts, we give 'yes' as an output.

**THEOREM 5.1:**  $H$  and  $h$  are not decidable

*Proof:* Assume  $H$  is decidable. Then there is an IT-computable function  $r$  such that  $r(p, x) = 1$  if  $\varphi_p(x) \downarrow$  and  $r(p, x) = 0$  otherwise. But then there is a program  $q$  that computes

$$\varphi_q(p) = \begin{cases} 1 & \text{if } r(p, p) = 0 \\ \text{undefined} & \text{otherwise.} \end{cases}$$

This implies that  $\varphi_q(q) \downarrow$  iff  $\varphi_q(q) = 1$  iff  $r(q, q) = 0$  iff  $\varphi_q(q) \uparrow$ . That is a contradiction, hence,  $H$  is not decidable.

Now assume  $h$  is decidable. That implies that  $\tilde{h} = \omega - h$  also is decidable. It is easy to see that these two sets are countable. We will now define a bijection  $g: h \rightarrow \tilde{h}$  simply by letting the  $n$ 'th element in  $h$  go to the  $n$ 'th element in  $\tilde{h}$ . Since both  $h$  and  $\tilde{h}$  are countable and decidable,  $g$  must be computable. Define  $f: \omega \rightarrow \omega$  simply by letting  $f(p) = g(p)$  if  $p \in h$  and  $f(p) = g^{-1}(p)$  otherwise. Since  $g$  is computable and  $h \cup \tilde{h} = \omega$ ,  $f$  is a total computable function on  $\omega$ . The recursion theorem implies that there is a program  $q$  such that  $\varphi_q = \varphi_{f(q)}$  and hence  $\varphi_q(0) \downarrow$  iff  $\varphi_{f(q)}(0) \downarrow$ . But by the definition of  $f$  we have that  $\varphi_q(0) \downarrow \Rightarrow q \in h \Rightarrow f(q) = g(q) \Rightarrow f(q) \in \tilde{h} \Rightarrow f(q) \notin h \Rightarrow \varphi_{f(q)}(0) \uparrow$ . This is a contradiction, hence,  $h$  is not decidable.  $\dashv$



Let  $H_\alpha = \{(p, x); p \text{ halts in less than } \alpha \text{ steps of computation on input } x\}$  and  $h_\alpha = \{p; p \text{ halts in less than } \alpha \text{ steps of computation on input } 0\}$ .

**THEOREM 5.2:** For every  $\alpha < \omega_1$  there is a program  $p$  and a real  $x$  such that  $p$  halts in  $\alpha$  many steps on input  $x$ .

*Proof:* Every ordinal  $\alpha$  can be written on the form  $\beta + \delta$  where  $\beta$  is a limit of limits ordinal (or 0) and  $\delta < \omega^2$  (Cantors normal form). Let  $x$  be such that  $\|x\| = \beta$  and let  $p$  be the program that first counts through  $x$  like in the proof of Theorem 3.3. This can be done in  $\|x\| = \beta$  many steps since  $\beta$  is a limit of limits. When this is done,  $p$  simply clocks  $\delta$  and halts.  $\dashv$

Hence, for all countable ordinals  $\alpha$  and  $\beta$  such that  $\alpha \neq \beta$  we have that  $H_\alpha \neq H_\beta$ .

But this does not hold for the  $h_\alpha$ 's. For example, if  $\alpha \notin \text{ClO}$  and  $\beta$  is the first clockable ordinal greater than  $\alpha$  we have that  $\alpha \neq \beta$  but  $h_\alpha = h_\beta$ . We also have that  $h_\alpha = h$  for all  $\alpha \geq \lambda$ .

**THEOREM 5.3:**  $H_\alpha$  and  $h_\alpha$  are decidable for every  $\alpha \in \text{WrO}$ .

*Proof:* First we show that  $H_\alpha$  is decidable. Consider the program that on input  $(p, x)$  first writes  $z$ , where  $\|z\| = \alpha$ , on a slice of the scratch-tape. This can be done since  $\alpha \in \text{WrO}$ . Then it starts to simulate the program  $p$  on input  $x$ , and for every step of computation in the simulation of  $p$ , it erases the least element in the well-order that  $z$  codes. If the simulation of  $p$  halts before this well-order is erased, it halts with output 'yes', and if the well-order is completely erased before  $p$  has halted, it halts with output 'no'. This decides if  $(p, x) \in H_\alpha$  or not. And of course, to decide if  $p \in h_\alpha$ , just check if  $(p, 0) \in H_\alpha$ , hence  $h_\alpha$  is also decidable.  $\dashv$

**THEOREM 5.4:**  $H_\lambda$  and  $h_\lambda$  are semi-decidable but not decidable.

*Proof:* That  $h_\lambda$  is semi-decidable but not decidable follows directly since  $h_\lambda = h$ . It is also easy to see that if  $H_\lambda$  is decidable, then so is  $h_\lambda$ , hence is  $H_\lambda$  not decidable either. So it suffices to show that  $H_\lambda$  is semi-decidable. But that is easy. On input  $(p, x)$ , just start simulating the program  $p$  on input  $x$ , and at the same time simulate every program on input 0 (like a universal machine). If the simulation of  $p$  halts, we wait for some of the other simulated programs on input 0 to halt, and if one of them does after  $p$  has halted, we halt with output 'yes'.  $\dashv$

**THEOREM 5.5:** For any limit ordinal  $\alpha \in \text{ClO}$ ,  $H_\alpha$  and  $h_\alpha$  are not  $\alpha$ -decidable, but they are  $\alpha$ -semi-decidable and  $(\alpha + 1)$ -decidable.

*Proof:* Consider the program  $q$  that on input  $(p, x)$  simulates the program  $p$  on input  $x$  while simultaneously clocking  $\alpha$ . This can be done in such a way that in  $\omega$  steps of computation of  $q$ ,  $\omega$  steps of computation will be simulated in both  $p$  and the  $\alpha$ -clock. If  $p$  halts before the  $\alpha$ -clock,  $q$  halts with output 'yes', but if the  $\alpha$ -clock halts, we can arrange for  $q$  to recognize this in the  $\alpha$ 'th step of computation

and halt with output ‘no’. The program  $q$  will halt in less than  $\alpha + 1$  steps of computation on every input and it decides  $H_\alpha$ . Hence  $H_\alpha$  and  $h_\alpha$  (to decide if  $p \in h_\alpha$  in less than  $\alpha + 1$  steps, just run  $q$  with input  $(p, 0)$ ) are  $\alpha + 1$ -decidable. But  $q$  also shows that  $H_\alpha$  and  $h_\alpha$  are  $\alpha$ -semi-decidable, since  $q$  halts with output ‘yes’ on input  $(p, x)$  in less than  $\alpha$  steps iff  $(p, x) \in H_\alpha$ . Now it remains to show that  $h_\alpha$  and  $H_\alpha$  are not  $\alpha$ -decidable. But the second part of the proof of Theorem 5.1, that shows that  $h$  is not decidable, is generalizable to state that  $h_\alpha$  is not  $\alpha$ -decidable, since for the program  $q$  produced by the recursion theorem in this proof, we have that  $q$  halts in less than  $\alpha$  steps iff  $f(q)$  halts in less than  $\alpha$  steps, so the contradiction still holds, hence  $h_\alpha$  is not  $\alpha$ -decidable. Similarly, if  $H_\alpha$  is  $\alpha$ -decidable, we can make sure that the program  $q$  in the first part of Theorem 5.1 halts in less  $\alpha$  steps, if it halts at all, so the contradiction still holds. Hence  $H_\alpha$  is not  $\alpha$ -decidable either.  $\dashv$

REMARK 5.6: The program  $q$  in the proof above needs to check that the first input  $p$  really is a natural number, and this we know from Theorem 2.5, takes  $\omega$  many steps. One could imagine that this could potentially mess up this algorithm when  $\alpha < \omega^2$ . But we can in fact do the first  $\omega$  steps in the algorithm described above and check such that  $p \in \omega$  at the same time, in  $\omega$  many steps. First we read  $p$  as if it was a natural number, that is, we search for the first 0 in  $p$ . When we find it (if we do not find a 0, we halt with output ‘no’ after  $\omega$  steps), we ‘assume’ that the rest of the first input-tape is filled with zeros. Then we check (with the help of a bunch of flags) that this is the case at the same time as we run the algorithm above with the potentially correct natural number. If we after  $\omega$  many steps discover that the rest of the first input-tape was not only 0’s, we halt with output ‘no’, and if it was, we are all good and continues the algorithm above.

DEFINITION 5.7A: A *snapshot* of a computation  $\varphi_p(x)$  at stage  $\alpha$  is a complete description of the machine that computes  $\varphi_p(x)$  after  $\alpha$  steps of computation, that is, a complete description of the three tapes at stage  $\alpha$ , and a description of where the head is, and which state the program is in, at stage  $\alpha$ . So a snapshot can be represented by an element in  $2^\omega \times 2^\omega \times 2^\omega \times \omega \times \omega$  (three reals to describe the tapes, two natural numbers to describe the location of the head and the current state), so it is easy to code snapshots with reals.

DEFINITION 5.7B: A *settled snapshot sequence* for a computation  $\varphi_p(x)$  is a sequence of snapshots such that the  $\alpha$ ’th snapshot in the sequence is a snapshot of  $\varphi_p(x)$  at stage  $\alpha$ , and furthermore, the last snapshot in the sequence is either in a halting stage or the first time that the computation repeat itself in the sense of Remark 2.2. Hence, every computation  $\varphi_p(x)$  has exactly one settled snapshot sequence, and it can be coded by a real, since it is a countable sequence of reals.

THEOREM 5.8 (NO UNIFORMIZATION THEOREM): There is a decidable set  $A \subseteq 2^\omega \times 2^\omega$  such that for every  $y \in 2^\omega$  there exists a  $z \in 2^\omega$  such that  $(y, z) \in A$  but such that  $A$  does not contain the graph of any computable total function.

*Proof:* Consider  $A = \{(\langle p, x \rangle, z); z \text{ codes a settled snapshot sequence for } \varphi_p(x)\}$ , where we choose the pairing function such that for every real  $y$  there is a real  $x$  and a natural number  $p$  such that  $y = \langle p, x \rangle$ .

$A$  is decidable since we on input  $(\langle p, x \rangle, z)$  can simulate  $\varphi_p(x)$  and check that the snapshot sequence that  $z$  codes corresponds to the simulated computation. If at some point we find a snapshot that does not correspond to the simulated computation we answer ‘no’. If this never happens one of two things can happen. Either the simulated computation halts, (and then we check if the corresponding snapshot is the last snapshot in the sequence), or we ‘run out’ of snapshots, (and then we check so that the last snapshot is the first snapshot where the computation repeats itself). Either way, it is decidable whether  $z$  codes a settled snapshot sequence for  $\varphi_p(x)$  or not.

For every  $y \in 2^\omega$  there is a  $x \in 2^\omega$  and a  $p \in \omega$  such that  $y = \langle p, x \rangle$  and since every computation has a settled snapshot sequence, there is a  $z \in 2^\omega$  such that  $z$  codes a settled snapshot sequence for  $\varphi_p(x)$ , and subsequently  $(y, z) \in A$ .

Assume now for a contradiction that  $A$  contains the graph of a total computable function  $f$ . We will specify a program that, with the help of  $f$ , decides  $H$ . On an input  $(p, x)$  the program simply simulates  $f(\langle p, x \rangle)$  and decodes the result  $z$  into the settled snapshot sequence that  $z$  codes and check if the last snapshot is in a halt-stage (halt with output ‘yes’) or in a repeat-stage (halt with output ‘no’).  $\dashv$

**COROLLARY 5.9:** There is a program  $p$  and a real  $x$  such that the settled snapshot sequence for  $\varphi_p(x)$  is not even accidentally  $x$ -writable<sup>6</sup>.

*Proof:* Consider the program that on input  $\langle p, x \rangle$  starts to simulate all computations on the form  $\varphi_q(x)$ , and after every step of computation of a simulated  $\varphi_q(x)$  it checks if any of the reals currently on  $q$ ’s simulated tapes are a settled snapshot sequence for  $\varphi_p(x)$  (this is decidable according to the No Uniformization Theorem). If it finds such a real, it is written to the output-tape and the program halts the machine. This defines a computable function  $f$  whose graph is contained in  $A$ , hence,  $f$  must not be total, because then  $f$  contradicts the No Uniformization Theorem. Hence, for at least one input  $\langle p, x \rangle$  the settled snapshot sequence will not be accidentally written by a computation with input  $x$ .  $\dashv$

**COROLLARY 5.10:** There is a total function, with a decidable graph, that is not computable.

*Proof:* Let  $f$  be a function such that, for every input  $\langle p, x \rangle$ ,  $f(\langle p, x \rangle) = z$ , where  $z$  is the settled snapshot sequence for  $\varphi_p(x)$ . Then the set  $A$  in (the proof of) the No Uniformization Theorem is the graph of  $f$ . Hence,  $f$  is a total function with a decidable graph, but  $f$  can not be computable since then  $A$  would contain the graph of a computable total function, which contradict the No Uniformization Theorem. (Another way of seeing why  $f$  is not computable is to use it to solve the halting problem.)  $\dashv$

**THEOREM 5.11 (LOST MELODY THEOREM):** There is a real  $c$  such that  $\{c\}$  is decidable but  $c \notin \text{WrR}$ .

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<sup>6</sup>A real is  $x$ -writable if it is writable by a machine with  $x$  as a real-oracle (to be defined in section 6) or equivalent, if it is writable with  $x$  as input (instead of 0).

*Proof:* For every  $p \in \omega$ , let  $z_p$  be a real coding the settled snapshot sequence for  $\varphi_p(0)$ . Let  $c$  be a real coding all the  $z_p$ 's by 'slicing' the real in  $\omega$  slices and have  $z_n$  on the  $n$ 'th slice. Assume for a contradiction that  $c \in \text{WrR}$ . We specify a program that decides  $h$ . On input  $p$ , it simply writes  $c$  to the scratch-tape and decodes the real on the  $p$ 'th slice (that is  $z_p$ ) into the settled snapshot sequence it codes and checks if the last snapshot is in a halt-stage (output 'yes') or in a repeat-stage (output 'no'). This program decides  $h$  which is a contradiction, hence  $c \notin \text{WrR}$ .

For every  $k \in \omega$  there is a  $\varphi_{p_k}$  that decides if the  $k$ 'th slice of its input is  $z_k$  by first decoding the  $k$ 'th slice of the input into (what could be) a settled snapshot sequence and then simulating  $\varphi_k(0)$  and check that the simulated computation correspond to the snapshot sequence (similar to what was done in the proof of the No Uniformization Theorem). Let a program  $q$  simulate all these  $\varphi_{p_k}$ 's and halt with output 'no' if one of them halts with output 'no' and halt with output 'yes' if all of them has halted with output 'yes'. Then  $\varphi_q$  decides  $\{c\}$ .  $\dashv$

**COROLLARY 5.12:** There is a constant total function, with a decidable graph, that is not computable.

*Proof:* Let  $f$  be a function such that, for every input  $x$ ,  $f(x) = c$ , where  $c$  is the real from the Lost Melody Theorem. Then  $f$  is a constant total function, with a decidable graph. Assume that  $f$  is computable, that is, there is a  $p \in \omega$  such that  $\varphi_p = f$ , then  $\varphi_p(0) = c$  which contradicts that  $c \notin \text{WrR}$ .  $\dashv$

Our next theorem implies that the real  $c$  in the Lost Melody Theorem is not even accidentally writable.

**THEOREM 5.13:** Every decidable set of reals that does not contain any writable real, does not contain any accidentally writable real.

*Proof:* Let  $A$  be a set of reals such that  $A \cap \text{WrR} = \emptyset$  but  $A \cap \text{AWrR} \neq \emptyset$ . We show that  $A$  is not decidable. Assume for a contradiction that  $A$  is decidable and consider a program  $q$  that simulates all computations on the form  $\varphi_p(0)$ , and after every step of computation in every simulated program  $q$  checks if any of the reals written to the simulated tapes in the simulated computation is in  $A$ . Since  $A \cap \text{AWrR} \neq \emptyset$ , such a real will eventually be found and then  $q$  copies this real to the output-tape and halts. Thus,  $\varphi_q(0)$  will halt with a real from  $A$  on the output-tape which is a contradiction since  $A \cap \text{WrR} = \emptyset$ .  $\dashv$

## 6. Oracles

There are two different kinds of oracles for IT-machines, oracles that are single reals, and oracles that are sets of reals. A machine with a real-oracle has an extra 'oracle-tape' where the real that is its oracle is written, just like an extra input. A machine with a set-oracle has an extra 'query-tape' where it can write down a real and (in one step of computation) get an answer if this real is in the oracle-set or not.

It is generally not equivalent to have  $x$  as a real-oracle or have  $\{x\}$  as a set-oracle. To see this, assume that  $x$  is not even accidentally writable. A machine with  $x$  as

a real-oracle can easily halt with  $x$  on the output-tape on input 0 (just consider the program that copies the oracle-tape to the output-tape and halts). But this can not a machine with  $\{x\}$  as a set-oracle do. To see why, notice that, since  $x \notin \text{AWrR}$ , no real can be written to the query-tape in a computation on input 0, that would generate a positive reply from the oracle. Hence, if a machine with  $\{x\}$  as an oracle halts with  $x$  on the output-tape on input 0, then there is a machine with no oracle that halts with  $x$  on the output-tape on input 0, that is,  $x \in \text{WrR}$ , but that is a contradiction.

We let  $\varphi_p^x$  be the function that is determined by running the program  $p$  with real-oracle  $x$ , and we let  $\varphi_p^A$  be the function determined by running the program  $p$  with set-oracle  $A$ .

For every real  $x$ , let  $M_x = \{n \in \omega; \text{the } n\text{'th digit in } x \text{ is } 1\}$ . It is easy to see that with  $x$  as a real-oracle, it is a simple task to decide if a real is in  $M_x$  or not. And with  $M_x$  as a set-oracle, it is easy to write down  $x$  and use it as a real-oracle. Hence, it is equivalent to have  $x$  as a real-oracle or  $M_x$  as a set-oracle. So in a sense, we could view the real-oracles as special cases of set-oracles, namely those consisting only of (reals coding) natural numbers.

Let  $A$  and  $B$  be oracles, then  $A \leq_\infty B$  ( $A$  is computable from  $B$ ) if the characteristic function of  $A$  is computable with  $B$  as an oracle. If we think of reals as subsets of  $\omega$ , this definition makes sense for all kinds of oracles. As usual we let  $A \equiv_\infty B$  if  $A \leq_\infty B$  and  $B \leq_\infty A$  and it is easy to see that this is an equivalence relation.

The notions of computability, decidability, clockability, writability and so forth are extended to  $A$ -computability,  $A$ -decidability, and so forth, in the natural way. For example, a function  $f$  is  $A$ -computable if  $f = \varphi_p^A$  for some  $p \in \omega$  and an ordinal  $\alpha$  is  $A$ -clockable if there is a program that uses  $A$  as an oracle that halts after  $\alpha$  steps of computation on input 0. Notice that  $A \leq_\infty B$  iff  $A$  is  $B$ -decidable. Notice also that for every real  $x$  and every oracle  $B$  we have that  $x \leq_\infty B$  iff  $M_x \leq_\infty B$  iff  $M_x$  is  $B$ -decidable iff  $x$  is  $B$ -writable.

Now we define two jump operators, the weak jump and the strong jump, corresponding to the two halting problems.

Let  $A \oplus B$  be the set of reals from  $A$  and  $B$  where we have added a 0 in front of all reals in  $A$  and 1 in front of all reals in  $B$ . Then both  $A$  and  $B$  are  $A \oplus B$ -decidable, and if  $A$  and  $B$  are  $C$ -decidable for some set of reals  $C$ , then  $A \oplus B$  is also  $C$ -decidable. Hence  $A \oplus B$  is in the  $\leq_\infty$ -smallest equivalence class that decides both  $A$  and  $B$ .

DEFINITION 6.1A: For any oracle  $A$ , the strong jump of  $A$ , denoted  $A^\blacktriangledown$  is the set  $\{(p, x); \varphi_p^A(x) \downarrow\}$ .

DEFINITION 6.1B: For any oracle  $A$ , the weak jump of  $A$ , denoted  $A^\blacktriangledown$  is the set  $A \oplus \{p; \varphi_p^A(0) \downarrow\}$ .

As we shall see in the next theorem there are sets of reals that are not decidable from any real, so the reason for why we include  $A$  in the definition of  $A^\blacktriangledown$  is to make sure that  $A \leq_\infty A^\blacktriangledown$ .

THEOREM 6.2:  $H$  is not computable from any real.

*Proof:* Assume for a contradiction that  $H \leq_\infty z$  for some real  $z$ , that is, there is a program  $q$  such that  $\varphi_q^z(p, x) = 1$  if  $\varphi_p(x) \downarrow$  and  $\varphi_q^z(p, x) = 0$  otherwise. Let  $f$  be the computable function (defined only on  $\omega$ ) that on an input  $r$ , computes  $s(q, r)$  (where  $s$  is such that  $\varphi_q^x(r, x) = \varphi_{s(q,r)}^x(x)$ ), decodes  $s(q, r)$  into the tuples describing the program and changes these tuples so that instead of halting,  $s(q, r)$  first checks if the output is 0, and if it is, it halts, otherwise it enters a never ending loop. Then  $f(r)$  is the program that halts on input  $x$  iff  $\varphi_{s(q,r)}^x(x) = \varphi_q^x(r, x) = 0$ . But since  $f$  is computable and total, there is a program  $r$  such that  $\varphi_r = \varphi_{f(r)}$ . But then we have  $\varphi_r(z) \downarrow$  iff  $\varphi_{f(r)}(z) \downarrow$  iff  $\varphi_{s(q,r)}^z(z) = 0$  iff  $\varphi_q^z(r, z) = 0$  iff  $\varphi_r(z) \uparrow$ . That is a contradiction.  $\dashv$

This also shows, as we promised earlier, that  $H \not\equiv_\infty h$ , since every set of natural numbers is equivalent to some real.

**THEOREM 6.3:** WrR is not decidable.

*Proof:* Assume WrR is decidable. Consider the universal machine that simulates  $\varphi_p$  for all  $p \in \omega$  and every time a real  $x$  is written to any of  $\varphi_p$ 's simulated tapes, it checks if  $x \in \text{WrR}$ . If it finds an  $x$  such that  $x \notin \text{WrR}$ , it copies  $x$  to the output-tape and halts. Since a non-writable real eventually will be written to one of  $\varphi_p$ 's tapes, this machine halts with a non-writable real on the output-tape on input 0. That is a contradiction.  $\dashv$

**COROLLARY 6.4:**  $A = \{(x, y); x \leq_\infty y\}$  is not decidable.

*Proof:* Since  $(x, 0) \in A$  iff  $x \in \text{WrR}$ , we have that if  $A$  is decidable, so is WrR. Hence, the result follows from Theorem 6.3.  $\dashv$

**THEOREM 6.5:** There are incomparable IT-degrees in the reals.

*Proof:* Assume that there are no incomparable IT-degrees in the reals, which implies that the degrees are linearly ordered. Since every initial segment of this order is countable (since there are only countable many programs), there are at most  $\aleph_1$  degrees, so the continuum hypothesis holds. But it can be shown that the assertion that there are incomparable degrees, is a  $\Sigma_2^1$ -assertion (see [1]), hence this assertion is absolute to every forcing extension due to the Shoenfield absoluteness theorem. But since there is a forcing extension where the continuum hypothesis is false, there must be incomparable degrees in this extension. Hence there are incomparable IT-degrees in the reals.  $\dashv$

## 7. Machines with only one tape (or a small head)

When we defined the IT-machines we allowed the head to read three cells at once, one from each tape. What happens if we allow the machines to read only one cell at the time? One could argue that this would be a more natural definition since the machine can write to only one cell at the time.

We start every computation with the head at the beginning of the input-tape and in the limit-stages, the head is placed at the beginning of the scratch-tape.

We would have to add two new commands for the head to be able to move up and down between the tapes.

It is fairly easy to see that these ‘1-read-machines’ are equally powerful as the IT-machines in the sense that every IT-computable function is computable by a 1-read-machine (and vice versa). But it might just take a finite amount of steps longer since we might have to move up and down to read from the three cells that an ordinary machine reads simultaneously (it is a finite amount of steps since the difference will disappear in the limit-stages). So the question is if the ordinals clockable by a 1-read-machines are the same as the ordinary clockable ordinals.

Let  $\text{ClO}_{1R}$  be the set of ordinals clockable by a 1-read-machine.

**THEOREM 7.1 (WEAK SPEED-UP):**  $\alpha + n \in \text{ClO}_{1R} \Rightarrow \alpha + 1 \in \text{ClO}_{1R}$  for all  $\alpha$  and all  $n \in \omega$ .

*Proof:* The proof is analog to the proof of the Speed-up Lemma for the IT-machines (Theorem 3.4) except that since we can not look at two cells at once we will be forced to move a step up or down to see the other flag. All the other extra steps we might have to make will disappear in the limit-stages.  $\dashv$

So, by the argument above, we have that  $\alpha \in \text{ClO} \Rightarrow \alpha + n \in \text{ClO}_{1R}$  for some  $n \in \omega$ , hence, by Theorem 7.1, we have that  $\alpha \in \text{ClO} \Rightarrow \alpha + 1 \in \text{ClO}_{1R}$ , and consequently, every clockable successor ordinal is 1-read-clockable.

Now we will first show that there is a gap of size exactly  $\omega^2$  in the clockable ordinals, and then show that the clockable ordinals ending gaps of limit of limits length, are in fact not 1-read-clockable, hence  $\text{ClO} \neq \text{ClO}_{1R}$ .

**THEOREM 7.2:** The first gap of size at least  $\omega^2$  in the clockable ordinals is of size exactly  $\omega^2$ .

*Proof:* We know from Theorem 3.6 that there is a gap of size at least  $\omega^2$  in the clockable ordinals. We will specify a universal machine that will halt after  $\alpha + \omega^2$  steps, where  $\alpha$  is the first ordinal in the first gap of size at least  $\omega^2$ . We slice up the tapes in  $\omega$  parts to simulate  $\varphi_p$  for every program  $p$ , but leaving the first cell on every tape for flags. We will use two flags,  $f_1$  and  $f_2$ . Every time a simulated program halts, we flash  $f_1$ , and if  $f_1 = 0$  in a limit-stage, we flash  $f_2$ . We halt in the first limit-stage where  $f_1 = 0$  and  $f_2 = 1$ .

Let  $f_i(\beta)$  be the value of  $f_i$  after  $\beta$  steps of computation.

Since no program halts between  $\alpha$  and  $\alpha + \omega^2$ , we have that  $f_1(\beta) = 0$  for all  $\beta$  such that  $\alpha + \omega \leq \beta < \alpha + \omega^2$ . Thus  $f_1$  has truly stabilized to 0 before the limit-stage  $\alpha + \omega^2$ , hence  $f_1(\alpha + \omega^2) = 0$ . Also  $f_1(\alpha + \omega) = 0$ ,  $f_1(\alpha + \omega \cdot 2) = 0, \dots$ , hence  $f_2$  changes value unboundedly often before stage  $\alpha + \omega^2$ , making  $f_2(\alpha + \omega^2) = 1$ . Therefore, as long as this machine does not halt at some stage  $\gamma \leq \alpha$ , this machine halts at  $\alpha + \omega^2$  and proves the theorem.

Let  $\gamma$  be any limit ordinal less than, or equal to  $\alpha$ . We show that  $f_2(\gamma) = 1 \Rightarrow f_1(\gamma) = 1$  which will complete the proof.

Assume that  $f_2(\gamma) = 1$ . Then we may assume that  $\gamma$  is a limit of countably many limit-stages where  $f_1 = 0$ , since otherwise there is a  $\beta < \gamma$  such that  $f_1$  is 1 for all stages between  $\beta$  and  $\gamma$ , and consequently  $f_1(\gamma) = 1$ . Since  $\gamma$  is a limit

of countably many limit-stages where  $f_1 = 0$ , there must be unboundedly large limit-stages less than  $\gamma$  where  $f_1 = 1$ , otherwise would  $\gamma$  end or be inside of a gap of size greater or equal to  $\omega^2$ , which contradicts the choice of  $\alpha$ . Thus we have that  $f_1(\gamma) = 1$ .  $\dashv$

It is promising that we use the machines ability to read two flags at once in this algorithm since we aim to prove that this ordinal is not 1-read-clockable.

**THEOREM 7.3:** Every ordinal  $\beta$  that ends a gap of limit of limits size in the clockable ordinals, is not 1-read-clockable.

*Proof:* Assume  $\beta \in \text{ClO}_{1R}$ . Since  $\beta$  is a limit ordinal, there has to be a program  $p$  that halts after it has read what is on the first cell  $C_0$  on the scratch-tape directly after the limit. It is easy to see, since  $\beta$  is a limit of limits, that  $C_0(\beta) \neq 0$ , since otherwise  $C_0$  would have stabilized at some stage  $\gamma < \beta$ , but the machine would then have halted at the next limit-stage (which can not be  $\beta$  since that limit-stage is not a limit of limits). Hence, this machine will halt in the first limit-stage where  $C_0$  is 1. With the same argument one realizes that  $C_0$  can not have stabilized to 1 either, it must have changed value unboundedly often before  $\beta$ .

For every  $n$ , there is a program  $p_{k_n}$  that simulates  $p$  but has made room for  $n$  flags that are all set to 0 at the start of the computation. After every step of computation in the simulated program  $p$ , it checks if the cell corresponding to  $C_0$  in  $p$  is 1, and every time it is, the first flag that are 0 is turned to 1. When all  $n$  flags are 1, the computation halts. If  $\alpha_n$  is the stage where  $C_0$  is 1 for the  $n$ 'th time in our original program, then  $p_{k_n}$  will halt at  $\alpha_n + k$  for some  $k \in \omega$ , so the Speed-up Lemma yields that  $\alpha_n \in \text{ClO}$  for all  $n \in \omega$ . Let  $\gamma = \sup\{\alpha_n; n \in \omega\}$ . Since  $\alpha_n < \alpha_{n+1}$  for all  $n \in \omega$ , we must have that  $\gamma$  is a limit ordinal. And since the  $\alpha_n$ 's are unbounded in  $\gamma$  and  $C_0(\alpha_n) = 1$  for all  $n \in \omega$ , we have that  $C_0(\gamma) = 1$ . Thus  $\beta = \gamma$  which contradicts that  $\beta$  ends a gap (since for every  $\alpha < \beta$  there is an  $\alpha_n \in \text{ClO}$  such that  $\alpha < \alpha_n < \beta$ ).  $\dashv$

This also shows that the Speed-up Lemma does not hold for 1-read-machines since for every  $\beta$  that ends a gap of limit of limits size in  $\text{ClO}$ , we have that  $\beta \in \text{ClO}$  and  $\beta + 1 \in \text{ClO}_{1R}$ , but  $\beta \notin \text{ClO}_{1R}$ . Thus the 1-read-machines has the same computational power as the IT-machines but  $\text{ClO}_{1R} \neq \text{ClO}$ .

How about an IT-machine with only one tape instead of three, a 1-tape-machine? Such a machine will have its only tape filled with the input at the start of the computation and when (if) it halts, the tape will be filled with the output.

In [2] Hamkins and Seabold shows that there is a 1-tape computable way of stretching the input so that it will be written to every third cell of the tape instead. They also show that when this is done, it is easy to simulate a given IT-machine by thinking of the tape as divided in three slices, with the input written to the first, and at the end of the computation, the output written to the third (with the second slice simulating the scratch-tape). They also show that there is a easy way of compressing this output to fill up the tape like it should at the end of a 1-tape computation.



Hamkins and Seabold also shows by a diagonalization argument in [2] that there is a function that is IT-computable but not 1-tape-computable.

This is confusing at first, but the 1-tape-computable functions are not generally closed under composition. To first stretch the input, then simulate the computation, then compress the output does not have to be a 1-tape-computable algorithm, even if the three parts of the algorithm is.

The reason for this is that the machine might not know that it should halt after the compression since we have no room for flags to tell us that we are done. In fact, the real at the tape after the compression might have already been written to the tape at some earlier stage of the computation.

They also show that we only need room for one single flag, that is, a cell that is not part of the output, for 1-tape-machines to compute the same functions as the IT-machines.

So how about the ‘1-tape-clockable’ ordinals  $\text{ClO}_{1T}$ ? In [2] it is shown that every clockable successor ordinal is 1-tape-clockable, which implies that the only 1-read-clockable ordinals that perhaps are not 1-tape-clockable, are the 1-read-clockable limit ordinals. Assume  $\alpha \in \text{ClO}_{1R}$  is a limit ordinal. Then it is easy to see that  $\alpha \in \text{ClO}_{1T}$ . Simulate the 1-read-computation that clocks  $\alpha$  on a 1-tape-machine and make sure that the first cell on the simulated scratch-tape is the first cell on the tape. This 1-tape-machine will halt at stage  $\alpha$  since the finite amount of steps more that the 1-tape-machine might have to make in every step of simulated computation will disappear in the limit-stages. Hence  $\text{ClO}_{1R} \subseteq \text{ClO}_{1T}$ . And by simply using only the scratch-tape it is easy to see that a 1-read-machine can clock every ordinal that a 1-tape-machine can. Hence  $\text{ClO}_{1T} \subseteq \text{ClO}_{1R}$  and subsequently  $\text{ClO}_{1T} = \text{ClO}_{1R}$ .

In [2] Hamkins and Seabold shows that every clockable limit of clockable ordinals is 1-tape-clockable. Which implies that the only clockable ordinals that might not be 1-tape-clockable are the ones that end gaps in the clockable ordinals. We know that every ordinal that ends a gap of limit of limits length in  $\text{ClO}$  is not 1-tape-clockable. So the only clockable ordinals that we do not know whether they are 1-tape-clockable or not are the ones that end gaps of size  $\beta + \omega$  for some  $\beta < \lambda$ .

It is an open question whether all the ordinals that ends gaps in  $\text{ClO}$  of length  $\beta + \omega$  where  $\beta < \lambda$  is 1-tape-clockable, however Hamkins and Seabold shows that many of these gaps is 1-tape-clockable in the following theorem. The proof is left out because we will prove a more general theorem later.

**THEOREM 7.4:** For every  $\delta, \beta \in \text{ClO}$ , the ordinal that ends the first gap after  $\delta$  of size at least  $\beta + \omega$  is 1-tape-clockable.

For more details on this, see [2].

**DEFINITION 7.5A:** Any gap of size  $\beta$  is a  $\Gamma_0$ -gap of size  $\beta$ .

**DEFINITION 7.5B:** A  $\Gamma_{n+1}$ -gap of size  $\beta$  is a gap of size  $\beta$  that begins with an ordinal that is a limit of ordinals that begins  $\Gamma_n$ -gaps of size at least  $\beta$ .

It is easy to see that the  $\Gamma_n$ -gaps with  $n \neq 0$  are not covered in 7.4 since there is no clockable ordinal  $\delta$  such that a  $\Gamma_n$ -gap (with  $n \neq 0$ ) of size  $\beta + \omega$  is the first

gap of size at least  $\beta + \omega$  after  $\delta$ , since this gap is a limit of such gaps (it is in fact, for every  $\gamma \in \text{CLO}$ , not even the  $\gamma$ 'th gap of size at least  $\beta + \omega$  after  $\delta$ ).

The first question is if there is a  $\Gamma_n$ -gap (with  $n \neq 0$ ) in the clockable ordinals. We will show that there is by specifying a program that would halt after the supremum of the clockable ordinals,  $\lambda$ , if no such gap existed.

Let  $G^\beta(n)$  be the formula: 'There is a universal 1-tape-machine with a flag  $f_n$  such that  $f_n = 0$  in a limit-stage iff it is in a stage that is  $\beta + \omega$  steps after the start of a  $\Gamma_n$ -gap of size at least  $\beta + \omega$ .'

Let  $F^\beta(n)$  be the formula: 'There is a universal 1-tape-machine with a flag  $\bar{f}_{n+1}$  such that  $\bar{f}_{n+1} = 1$  in a limit-stage iff it is in a stage that is a limit of  $\Gamma_n$ -gaps of size at least  $\beta + \omega$ .'

LEMMA 7.6: For every  $\beta \in \text{CLO}$ ,  $G^\beta(n)$  and  $F^\beta(n)$  holds for every  $n \in \omega$ .

*Proof:* We may assume that  $\beta$  is a successor ordinal since  $(\beta+1)+\omega = \beta+(1+\omega) = \beta + \omega$ . Hence, it is no restriction to assume that  $\beta \in \text{CLO}_{1T}$ .

We will show the lemma by an inductive argument over  $F$  and  $G$  simultaneously. We will show, for an arbitrary  $\beta \in \text{CLO}_{1T}$ , that

- (I)  $G^\beta(0)$  is true,
- (II) if  $G^\beta(n)$  is true, then  $F^\beta(n)$  is true,
- (III) if  $F^\beta(n)$  is true, then  $G^\beta(n+1)$  is true.

(I), (II) and (III) shows the lemma.

(I): We will not show this in the easiest way, since we will generalize this proof in (III). We need two flags,  $f_0$  and  $\bar{f}_0$ . We will make sure that  $f_0 = 0$  in a limit-stage iff we are  $\beta + \omega$  steps into a gap of size at least  $\beta + \omega$ , and consequently proving (I). We define an universal machine with a  $\beta$ -clock (that is, a simulated computation that halts in  $\beta$  many steps) that makes  $\omega$  steps of computation of every simulated computation and the  $\beta$ -clock, in  $\omega$  many steps.

*When a simulated computation halts:* Set  $\bar{f}_0$  to 1 and set  $f_0$  to 1.

*In a limit:* If  $\bar{f}_0$  is 1, then restart the  $\beta$ -clock (see Remark 7.7) and set  $\bar{f}_0$  to 0, if  $f_0$  is 0, set it to 1.

*When the  $\beta$ -clock halts:* set  $f_0$  to 0.

Let  $\beta'$  be the limit ordinal such that  $\beta = \beta' + n$  for some  $n \in \omega$ . Then it is easy to see that when the  $\beta$ -clock halts, we are in a gap of size at least  $\beta'$  (since when a simulated computation halts, we reset the  $\beta$ -clock in the next limit). This is the only time that  $f_0$  is set to 0, so the only chance for  $f_0$  to be 0 at a limit-stage is if no computation halts before the next limit. But this happens if and only if we are  $\beta' + \omega = \beta + \omega$  steps into a gap of size at least  $\beta + \omega$  which is exactly what we want.

(II): Consider the machine promised to exist by  $G^\beta(n)$ . Then we know that  $f_n = 0$  in a limit-stage exactly one time in every  $\Gamma_n$ -gap of size at least  $\beta + \omega$  and never at any other limit-stage. So flash a new flag  $\bar{f}_{n+1}$  in every limit-stage where

$f_n = 0$ . When  $\bar{f}_{n+1} = 1$  in a limit-stage, set it to 0. Then  $\bar{f}_{n+1}$  is 1 in a limit-stage iff it is a limit of  $\Gamma_n$ -gaps of size at least  $\beta + \omega$ .

(III): Consider the machine promised to exist by  $F^\beta(n)$ . This machine has a flag  $\bar{f}_n$  that is 1 in a limit-stage iff this limit-stage is a limit of  $\Gamma_n$ -gaps of size at least  $\beta + \omega$ . This implies that all gaps that starts when  $\bar{f}_n$  is 1 are  $\Gamma_{n+1}$ -gaps, and no gap that starts when  $\bar{f}_n$  is 0 is a  $\Gamma_{n+1}$ -gap of size at least  $\beta + \omega$ . So add a new flag,  $f_n$ , and let it follow the following instructions (similar to the machine in (I)):

*When a simulated computation halts:* Set  $f_n$  to 1.

*In a limit:* If  $\bar{f}_n$  is 1, then re-start the  $\beta$ -clock (see Remark 7.7) and set  $\bar{f}_n$  to 0, if  $f_n$  is 0, set it to 1.

*When the  $\beta$ -clock halts:* Set  $f_n$  to 0.

To see that these instructions gives us the machine we want is similar to (I).  $\dashv$

REMARK 7.7: There could be a problem with the  $\beta$ -clock, due to the restarts. When we start the clock, the slice where we clock  $\beta$  could be full of old ‘crap’ from earlier clockings or limits of clockings. This could potentially mess up the clock since it is a computation on input 0. This is however solvable.

*Case 1,  $\beta < \omega$ :* It is easy to see that it yields an equivalent machine to skip the clock and just set  $f_n$  to 0 in every limit-stage.

*Case 2,  $\beta \geq \omega^2$ :* Let  $p$  be a program that clocks  $\beta$  and let  $q$  be the program that in  $\omega$  steps writes a 0 to every cell, regardless of how the tape was set up. Now let the real  $\beta$ -clock be the program that first runs  $q$  and then  $p$ . This program will halt in  $\omega + \beta = \beta$  many steps on every input.

*Case 3,  $\omega \leq \beta < \omega^2$ :* There is a clockable  $\beta'$  such that  $\omega + \beta' = \beta$ . So in this case the  $\beta$ -clock is the program that first runs  $q$  and then runs the program that clocks  $\beta'$ . This program will halt in  $\omega + \beta' = \beta$  many steps on every input.

Hence, for every clockable  $\beta$  there is a program that ends in  $\beta$  many steps of computation on every input.

THEOREM 7.8: For every  $n \in \omega$  and every  $\beta, \gamma \in \text{ClO}$ , there are  $\gamma$  many  $\Gamma_n$ -gaps of size at least  $\beta$  in ClO.

*Proof:* We show this with an inductive argument. We know from section 3 that, for every  $\gamma, \beta \in \text{ClO}$ , there are  $\gamma$  many  $\Gamma_0$ -gaps of size at least  $\beta$  in ClO so the case  $n = 0$  is already proved. Now assume that, for every  $\gamma, \beta \in \text{ClO}$ , there are  $\gamma$  many  $\Gamma_n$ -gaps of size at least  $\beta$  in ClO. Then the supremum of the clockable ordinals,  $\lambda$ , must be a limit of  $\Gamma_n$ -gaps of size at least  $\beta$ , since for every ordinal  $\mu < \lambda$ , there is a clockable  $\gamma > \mu$  such that there are  $\gamma$  many  $\Gamma_n$ -gaps of size at least  $\beta$ . Thus, at least one of these gaps must occur between  $\mu$  and  $\lambda$ . Hence, the flag  $\bar{f}_{n+1}$  in the  $F^\beta(n)$ -machine specified above is 1 at stage  $\lambda$ . If there are no  $\Gamma_{n+1}$ -gaps,  $\lambda$  would be the first limit-stage where this flag is 1 and we could easily direct the machine to halt when this happens. That is a contradiction since  $\lambda \notin \text{ClO}$ . Hence for every  $\beta \in \text{ClO}$  there is a  $\Gamma_{n+1}$ -gap of size at least  $\beta$  in ClO. Now assume for a contradiction that for a certain  $\beta$  it is not the case that for every  $\gamma \in \text{ClO}$  there are  $\gamma$  many  $\Gamma_{n+1}$ -gaps of size at least  $\beta$ . Then there must be exactly  $\eta$  such gaps

for an  $\eta < \lambda$ . But then let  $\sigma$  be the supremum of the lengths of all these  $\eta$  gaps. Obviously  $\sigma < \lambda$  so there is a  $\beta' \in \text{ClO}$  such that  $\sigma < \beta' < \lambda$ . But, we know there is a  $\Gamma_{n+1}$ -gap of size at least  $\beta'$  in ClO. This gap can not be any of the  $\eta$  gaps, since it is ‘bigger’ than every one of these. This is a contradiction since this is a gap of size at least  $\beta$ , and we can (finally) draw the conclusion that, for every  $\beta, \gamma \in \text{ClO}$ , there are  $\gamma$  many  $\Gamma_{n+1}$ -gaps of size at least  $\beta$ , which concludes the proof.  $\dashv$

**THEOREM 7.9:** If  $\alpha$  ends the  $\gamma$ 'th  $\Gamma_n$ -gap of size at least  $\beta + \omega$  after  $\delta$ , where  $\gamma, \beta, \delta \in \text{ClO}$  and  $n \in \omega$ , then  $\alpha \in \text{ClO}_{1T}$ .

*Proof:* We may assume that  $\delta$  and  $\beta$  are clockable successor ordinals, since the first gap after  $\delta$  is the same gap as the first gap after  $\delta + 1$ , and since  $(\beta + 1) + \omega = \beta + (1 + \omega) = \beta + \omega$ . So it is no restriction to assume that  $\beta$  and  $\delta$  is 1-tape-clockable.

Consider the 1-tape universal machine whose existence is promised by  $F^{\beta}(n)$ . This machine has a flag,  $f_n$ , that is 0 in a limit-stage iff this limit-stage is  $\beta + \omega$  steps into a  $\Gamma_n$ -gap of size at least  $\beta + \omega$ . It will also have a flag  $\bar{f}_n$  that is 1 in a limit-stage iff this limit-stage is a limit of  $\Gamma_n$ -gaps of size at least  $\beta + \omega$ . We will add a new flag  $\hat{f}$  that will be set to 1 at the start of the computation and set to 0 when the next  $\Gamma_n$ -gap of size at least  $\beta + \omega$  will be the  $\gamma$ 'th such gap. This is how:

At the start of the computation: Make one ‘IT-step’ (see Remark 7.10) of computation in the simulated IT-program  $p$  that clocks  $\gamma$  and set  $\hat{f}$  to 1.

When  $p$  halts: Set  $\hat{f}$  to 0.

In a limit: If  $\bar{f}_n = 1$  or if  $\hat{f} = 1$  and  $f_n = 0$  we make another ‘IT-step’ of computation in the simulation of  $p$ .

The first limit-stage where  $\hat{f} = f_n = 0$  is  $\alpha$ , since this is  $\beta + \omega$  steps into the  $\gamma$ 'th  $\Gamma_n$ -gap of size at least  $\beta + \omega$  and we could easily direct an IT-machine to halt in the first limit-stage when  $\hat{f} = f_n = 0$  since it can read three cells at once. But it is an easy thing to modify this into a 1-tape-machine by setting a master flag  $f$  to  $\max(\hat{f}, f_n)$  every time we change the value of  $\hat{f}$  or  $f_n$ . Then  $f = 0$  in a limit-stage iff  $\hat{f} = f_n = 0$  in this limit-stage. So by placing this master flag on the first cell of the tape, we can easily halt at stage  $\alpha$  and the proof is complete.  $\dashv$

**REMARK 7.10:** If an IT-program  $p$  clocks an  $\alpha \in \text{ClO}$ , we can make this into a 1-tape-computation simply by slicing the tape into three, and simulate the three tapes of this IT-machine on these slices. Since the input is 0, there is no need to stretch it, and we do not care about the output in this particular case so there is no need for compressing the output either. If we by an ‘IT-step’ of computation mean the finite amount of steps this 1-tape-machine does to simulate one step of computation in the original IT-machine (it has to move around and read from three cells before it consults the IT-program  $p$ ). Then it is easy to see that this 1-tape-computation will halt after  $\alpha$  ‘IT-steps’ of computation.

The proof of Theorem 7.9 shows that for every  $\beta, \gamma, \delta \in \text{ClO}$ , the  $\gamma$ 'th  $\Gamma_n$ -gap of size at least  $\beta + \omega$  after  $\delta$  is of size exactly  $\beta + \omega$ . Hence every  $\Gamma_n$ -gap of size greater than  $\beta + \omega$  is the  $\bar{\gamma}$ 'th gap of size at least  $\beta + \omega$ , after  $\delta$ , for some  $\bar{\gamma} \notin \text{ClO}$ .

There are (at least) two things that might cause Theorem 7.9 to fail to cover all the gaps we want it to.

1: The theorem does not cover any gaps of non-clockable length. Thus, one needs to show that no such gaps exists or one needs to strengthen the theorem.

2: There could be  $\Gamma_\alpha$ -gaps for  $\omega \leq \alpha < \lambda$ . If so, one needs to generalize the theorem to a more general induction.

## 8. Infinite time machines with infinite programs (ISIT-machines)

If we in the definition of the IT-machines let go of the restriction that the programs must be finite sets and instead let the programs be countable sets, we get the ISIT-machines. If we dropped the restriction that the ISIT-programs must be countable, it would not yield a more powerful machine (see Remark 8.7).

Thus, a program is a countable set of tuples  $\langle s, r, c, s' \rangle$  that specifies the machines actions. It is obvious that the IT-machines are those ISIT-machines with a finite program.

It is no restriction to let the set of all possible states be  $\omega$  since there can only be countably many states per program. So the set of possible tuples are exactly the same as for the IT-programs. Hence there is a recursive bijection between the set of possible tuples and  $\omega$ . So every program is coded by a subset of  $\omega$  (and every subset of  $\omega$  codes a program). And since every real codes a subset of  $\omega$  in an obvious way we will be careless, as usual, and call a real  $x$  a program when we really mean the program that  $x$  codes.

And, like before, every program  $x$  determines a partial function  $\varphi_x$  by letting the value of  $\varphi_x(z)$  be what is on the output-tape when the machine with program  $x$  and input  $z$  halts, and letting  $\varphi_x(z)$  be undefined when the machine does not halt. Of course  $\varphi_x = \varphi_y$  iff  $\varphi_x(z) \downarrow \leftrightarrow \varphi_y(z) \downarrow$  for all  $z$  and  $\varphi_x(z) = \varphi_y(z)$  for all  $z$  such that  $\varphi_x(z) \downarrow$ .

We extend, in the natural way, the notions of computability, (semi-)decidability, writability, and so forth, to the ISIT-context. For instance, a function  $f$  on reals is ISIT-computable if there is a program  $x$  such that  $f = \varphi_x$ .

**THEOREM 8.1:** Every real  $x$  is ISIT-writable in  $\omega$  steps of computation.

*Proof:* Fix an  $x \in 2^\omega$ . We show that there is a machine that writes  $x$  on the output-tape on input 0. Consider this program:

$\{\langle 2n+1, r, \text{write the } n\text{'th digit of } x \text{ on the output-tape, } 2n+2 \rangle; n \in \omega, r \in R\} \cup \{\langle 2n+2, r, \text{move one step to the right, } 2n+3 \rangle; n \in \omega, r \in R\}$  (where  $R$  is the set of possible reads). This machine writes  $x$  on the output-tape in  $\omega$  steps, and in the limit-stage it halts since there is no state 0 in the program.  $\dashv$

Hence ‘writable’ is a meaningless concept for ISIT-machines. We also see that the ISIT-machines are ‘stronger’ than the IT-machines, since Theorem 8.1 shows that there are uncountably many ISIT-computable functions.

**COROLLARY 8.2:** There are  $2^{\aleph_0}$  different ISIT-computable functions.

*Proof:* Since every program is coded by a real, there must be at most  $2^{\aleph_0}$  different ISIT-computable functions, but Theorem I implies that there are at least  $2^{\aleph_0}$  different ISIT-computable functions, so there must be exactly  $2^{\aleph_0}$  different ISIT-computable functions.  $\dashv$

**THEOREM 8.3:** For every ISIT-computable function  $f$  there are  $2^{\aleph_0}$  different indices  $x$  such that  $f = \varphi_x$ .

*Proof:* Let  $x$  be a program. Multiply every state in  $x$  (except 0 and 1) with 2. This gives us a new program  $x'$  such that  $\varphi_x = \varphi_{x'}$  with  $\omega$  many free states. So we can add pointless states to this program in  $2^\omega$  different ways. All these programs produce different indices but defines the same function.  $\dashv$

**THEOREM 8.4 (S-N-M):** There is an ISIT-computable, total function  $s$  such that  $\varphi_x(\bar{y}, \bar{z}) = \varphi_{s(x, \bar{y})}(\bar{z})$

*Proof:* Assume we have a program  $x$  with  $n + m$  arguments. We will specify the program  $x' = s(x, \bar{y})$  with  $m$  arguments. First it slices up the scratch-tape in  $n + 1$  slices and writes the  $\bar{y}$ 's on  $n$  of them, leaving one for scratch work. Then it runs the same algorithm as  $x$  except that it reads the  $\bar{y}$ 's from slices of the scratch-tape instead of input-tapes, and it does scratch work on a slice of the scratch-tape instead of the whole scratch-tape. This procedure is ISIT-computable. So for an ISIT-program that gets  $x$  and  $\bar{y}$  as input it is a easy thing to decode  $x$  into tuples, make these changes, and code it back into  $x' = s(x, \bar{y})$ . Hence,  $s$  is ISIT-computable.  $\dashv$

**THEOREM 8.5 (RECURSION):** For every total ISIT-computable function  $f$  there is a program  $x$  such that  $\varphi_x = \varphi_{f(x)}$ .

*Proof:* The classical proof works in this context as well. Fix  $f$ . Let  $\mu$  be the program so that  $\varphi_\mu(y, z) = \varphi_{f(s(y, y))}(z)$ . Let  $x = s(\mu, \mu)$ . Then we have  $\varphi_x(z) = \varphi_{s(\mu, \mu)}(z) = \varphi_\mu(\mu, z) = \varphi_{f(s(\mu, \mu))}(z) = \varphi_{f(x)}(z)$ .  $\dashv$

**THEOREM 8.6:** Every halting ISIT-computation is countable.

*Proof:* It is not hard to see that Remark 2.2 is true for ISIT-machines as well, so the proof of the corresponding theorem for IT-machines (Theorem 2.3) works in this context.  $\dashv$

**REMARK 8.7:** If we allowed the programs to be any set of tuples without the restriction that it must be countable, and let any ordinal be a possible state (instead of letting  $\omega$  be the set of possible states), it would not yield a more powerful machine. Remark 2.2 and Theorem 2.3 would be true in this context as well, making every halting computation countable and every non-halting computation caught in a countable loop. Thus, only a countable subset of the program is ever used, the rest is just dummy-tuples, and subsequently, only countably many states is used. Hence, every such machine can be simulated by an ISIT-machine.

THEOREM 8.8: Every ordinal  $\alpha < \omega_1$  is clockable.

*Proof:* Just like for IT-machines, it is easy to see that all  $\alpha < \omega^2$  is clockable (see section 3). Assume  $\alpha \geq \omega^2$ . According to Theorem 5.2 there is an IT-machine  $p$  that halts after  $\alpha$  steps of computation on some input  $x \in 2^\omega$ . Construct a ISIT-program that on input 0 first writes  $x$  on the input-tape and then runs the exact same algorithm as  $p$ . This machine will halt after  $\omega + \alpha = \alpha$  steps on input 0.  $\dashv$

Theorem 8.6 and Theorem 8.8 tell us that all ordinals that can be clockable is, hence ‘clockable’, just like ‘writable’, is not a meaningful concept for ISIT-machines. It is also easy to see that the set of writable ordinals is the same as the set of clockable ordinals.

Let  $A \hat{\leq}_\infty B$  if the characteristic function of  $A$  is ISIT-computable with  $B$  as an oracle, and let  $A \hat{\equiv}_\infty B$  if  $A \hat{\leq}_\infty B$  and  $B \hat{\leq}_\infty A$ . Of course, since every real is ISIT-writable, only set-oracles are meaningful in the ISIT context.

Let  $\hat{H}$  and  $\hat{h}$  be the halting problems relativized to ISIT-machines, that is,  $\hat{H} = \{(x, y); \varphi_x(y) \downarrow\}$  and  $\hat{h} = \{x; \varphi_x(0) \downarrow\}$ . It is easy to see that  $\hat{H}$  and  $\hat{h}$  are semi-decidable.

THEOREM 8.9:  $\hat{H} \hat{\equiv}_\infty \hat{h}$ .

*Proof:* Obviously  $\hat{h} \hat{\leq}_\infty \hat{H}$ . We show that  $\hat{H} \hat{\leq}_\infty \hat{h}$ . Suppose we want to decide if  $(x, y) \in \hat{H}$ , that is if  $\varphi_x(y) \downarrow$ . Since  $y$  is writable, there is a program  $f(x, y)$  such that  $f$  is computable and the program  $f(x, y)$ , on input 0, first writes  $y$  on a slice of the scratch-tape and then runs (practically) the same algorithm as  $x$ . Then we have that  $\varphi_x(y) \downarrow$  iff  $\varphi_{f(x, y)}(0) \downarrow$  and this we can decide with  $\hat{h}$  as an oracle.  $\dashv$

THEOREM 8.10:  $\hat{H}$  and  $\hat{h}$  are not ISIT-decidable.

*Proof:* The corresponding proof of that  $H$  is not IT-decidable (Theorem 5.1) works in this context to show that  $\hat{H}$  is not ISIT-decidable. Thus, Theorem 8.9 implies that  $\hat{h}$  is not ISIT-decidable. (The corresponding proof of that  $h$  is not IT-decidable does not work in this context since it uses that  $h$  is countable).  $\dashv$

Let  $\hat{H}_\alpha = \{(x, y); \text{the program } x \text{ halts in less than } \alpha \text{ steps of computation on input } y\}$  and  $\hat{h}_\alpha = \{(x, y); \text{the program } x \text{ halts in less than } \alpha \text{ steps of computation on input } 0\}$ . Then we have:

- (1)  $\hat{H}_\alpha \not\subseteq \hat{H}_\beta$ , for all  $\alpha < \beta \leq \omega_1$
- (2)  $\hat{H}_{\omega_1} = \hat{H}_\alpha = \hat{H}$ , for all  $\alpha \geq \omega_1$
- (3)  $\hat{H}_\alpha$  is ISIT- $\alpha$ -semi-decidable but not ISIT- $\alpha$ -decidable, for all limit  $\alpha$
- (4)  $\hat{H}_\alpha$  is ISIT- $(\alpha + 1)$ -decidable, for all limit  $\alpha$
- (5)  $\hat{H}_\alpha$  is ISIT-decidable for all  $\alpha < \omega_1$ .

(1) follows from Theorem 8.8, (2) follows from Theorem 8.6 and (3)-(5) has basically the same proof as corresponding IT-statements (see section 5). (1)-(5) also holds for  $\hat{h}$ .

**THEOREM 8.11:** There is an IT-program  $q$  such that for every ISIT-program  $x$  we have  $\varphi_x = \varphi_q^x$ .

*Proof:* We specify the IT-program  $q$ , that with  $x$  as a real-oracle, will simulate the ISIT-program  $x$ .

At ‘time’  $v$  (the ‘time’  $v$  here will correspond to the state the simulated computation currently is in):

The first time  $q$  does this, it does it with  $v = 1$  and after that  $q$  knows what  $v$  is from the previous round of computation. The first thing it does is ‘memorize’ what it reads  $r$  from the tape and then it starts decoding  $x$  (which it has access to since  $x$  is its oracle) into the tuples describing  $x$ ’s program. If it finds a tuple  $\langle v, r, c, w \rangle$  (where  $v$  is the current ‘time’), it moves back to where it read  $r$  and executes command  $c$ , flashes a flag on and of, and starts over (unless  $c$  tells it to halt) at ‘time’  $w$ . This will be done in a finite amount of steps (as long as it finds the corresponding tuple).

In limit-stages it checks if the flag is on or not. If it is on it will start over at ‘time’ 0, if it is off, that means that at some point we did not find the corresponding tuple and searched for it in  $x$  in vain, so then we halt. This IT-program  $q$  will, on every input  $\bar{y}$ , halt with  $\varphi_x(\bar{y})$  as output if  $\varphi_x(\bar{y})$  halts, and run forever if  $\varphi_x(\bar{y})$  is undefined, so  $\varphi_x = \varphi_q^x$ .  $\dashv$

From now on  $q$  will be the IT-program in 8.11 that simulates the ISIT-program that it has as a real-oracle.

**COROLLARY 8.12:** The power of ISIT-machines are exactly the same as the power of IT-machines with real-oracles.

*Proof:* Theorem 8.11 shows that the ISIT-machines are not stronger than IT-machines with real-oracles. To see that IT-machines with real-oracles are not stronger than the ISIT-machines, consider for every IT-program  $p$  with  $x$  as an oracle, the ISIT-program that first writes  $x$  on a slice of the scratch-tape and then runs basically the same algorithm as  $p$ .  $\dashv$

The proof of 8.11 uses the easy coding we have chosen to go from a real  $x$  to the program  $x$  codes, and back. One could imagine that this proof might not go through with an ISIT-computable coding-algorithm that is not IT-computable, but Corollary 8.12 still holds since the strength of the machines does not depend on the coding.

**COROLLARY 8.13:** For every ISIT-computable function  $\varphi_x$  where  $x$  is IT-writable there is an IT-computable function  $\varphi_p$  such that  $\varphi_x = \varphi_p$ .

*Proof:* First we write  $x$  on a slice of the scratch-tape and then we follow practically the same algorithm as in the proof of Theorem 8.11.  $\dashv$



This implies for example that the recursive ISIT-machines are not stronger than the IT-machines.

Let  $H = \{(x, y); \varphi_x(y) \downarrow \wedge x \text{ codes a finite program}\}$  and  $h = \{x; \varphi_x(0) \downarrow \wedge x \text{ codes a finite program}\}$ . So  $H$  and  $h$  are (equivalent to) the halting problems for the IT-machines.

COROLLARY 8.14:  $H$  is not ISIT-decidable.

*Proof:* Assume  $\varphi_x$  decides  $H$ . Theorem 8.11 implies that  $\varphi_q^x$  decides  $H$ . But that contradicts Theorem 6.2.  $\dashv$

THEOREM 8.15:  $\widehat{H} \cong_{\infty} H$ .

*Proof:* Since it is easy to check if  $x$  codes a finite program, it is clear that  $H \widehat{\leq}_{\infty} \widehat{H}$ . Now we show that  $\widehat{H} \widehat{\leq}_{\infty} H$ . Let  $q'$  be the program such that  $\varphi_{q'}(x, y) = \varphi_q^x(y)$ . This program exists since it is easy to direct  $q$  to look at the first input-tape instead of the oracle-tape. Then let  $q''$  be such that  $\varphi_{q'}(x, y) = \varphi_{q''}(\langle x, y \rangle)$ . But then we have that  $(x, y) \in \widehat{H}$  iff  $\varphi_x(y) \downarrow$  iff  $\varphi_q^x(y) \downarrow$  iff  $\varphi_{q'}(x, y) \downarrow$  iff  $\varphi_{q''}(\langle x, y \rangle) \downarrow$  iff  $(q'', \langle x, y \rangle) \in H$ .  $\dashv$

Since  $\widehat{H} = \{(x, y); \varphi_q^x(y) \downarrow\}$ , we see that the proof above also shows that  $\widehat{H} \equiv_{\infty} H$  (where  $\equiv_{\infty}$  is the relation defined in section 6).

THEOREM 8.16: The relation  $x \leq_{\infty} y$  is not ISIT-decidable (where  $\leq_{\infty}$  is the relation defined in section 6).

*Proof:* Assume there is an ISIT-program  $z$  such that  $\varphi_z$  decides  $A = \{(x, y); x \leq_{\infty} y\}$ . Then we have that  $\varphi_q^z$  decides  $A$ . But then there is an IT-program  $\bar{q}$  such that  $\varphi_{\bar{q}}^z(x) = \varphi_q^z(x, z)$ . This implies that  $B = \{x; x \leq_{\infty} z\} = \{x; x \text{ is } z\text{-writable}\}$  is IT-decidable with  $z$  as an oracle since  $\varphi_{\bar{q}}^z$  decides  $B$ . This is a contradiction. To see why, consider the IT-program  $q'$  that, with  $z$  as an oracle, simulates every computation on the form  $\varphi_p(z)$  on a slice of the scratch-tape, and as soon as a real on any of the simulated tapes have been altered,  $q'$  checks if this real is a member of  $B$ . If it is,  $q'$  continues with the computation, but if it is not,  $q'$  copies this real to the output-tape and halts. This machine will eventually halt with a real that is not  $z$ -writable on the output-tape (to see why, generalize Theorem 3.12 to show that there are eventually  $z$ -writable reals that are not  $z$ -writable). That is a contradiction.  $\dashv$

THEOREM 8.17: Every countable set of reals is ISIT-decidable.

*Proof:* For every countable set there is a program that slices up the scratch-tape in  $\omega$  slices and simply writes up all the members of the set. It is then a simple task to check if a given input has been written to any of the slices.  $\dashv$

COROLLARY 8.18:  $h$  is ISIT-decidable.

*Proof:* Follows directly since  $h$  is countable. Another way of seeing it is to consider the program that writes the supremum of the IT-clockable ordinals  $\lambda$  and then simulates the IT-machine in question on input 0 and at the same time counts down  $\lambda$ . The result also follows from the fact that  $c$  from the Lost Melody Theorem is ISIT-writable.  $\dashv$

COROLLARY 8.19: CIO, WrO, EWrO, AWrO, WrR, EWrR and AWrR are all ISIT-decidable.

*Proof:* Since  $\text{CIO} \subseteq \text{WrO} \subseteq \text{EWrO} \subseteq \text{AWrO}$  and  $\text{WrR} \subseteq \text{EWrR} \subseteq \text{AWrR}$  and  $|\text{AWrO}| \leq |\text{AWrR}|$ , it suffices to show that AWrR is countable and the result follows from Theorem 8.17. But that follows from Theorem 2.3 and Corollary 2.4, since they imply that every program produces at most countably many accidentally writable reals.  $\dashv$

THEOREM 8.20: Every ISIT-decidable and ISIT-semi-decidable set is  $\underline{\Delta}_2^1$ .<sup>7</sup>

*Proof:* Assume that  $\varphi_x$  (semi-)decides  $A$ . This means that  $\varphi_q^x$  (semi-)decides  $A$ . This means that  $A$  is IT-(semi-)decidable with  $x$  as a real-oracle. This means (according to section 7 in [1]) that  $A$  is  $\underline{\Delta}_2^1$ .  $\dashv$

## 9: Conclusions

In the proof of that  $\text{CIO} \subseteq \text{WrO}$ , we discover that Statement 4.4 is used (although never stated) without proof in [3]. It may very well be obvious that Statement 4.4 is true and that we just have failed to see it, otherwise, one would need to prove it to complete the proof.

We did not succeed in solving the open question in [2], that is, ‘is every ordinal that ends a gap of size  $\beta + \omega$  (for some  $\beta < \lambda$ ) in the clockable ordinals 1-tape-clockable?’, but at least we showed that even more of them, than was known before, are.

Thanks to Theorem 8.11, most of the questions we had about the ISIT-machines were answered with relative ease. We showed that the power of the ISIT-machines is equivalent to the power of IT-machines with real-oracles (in the sense that the same functions are computable). We also showed that it suffices for the ISIT-program to be IT-writable for the resulting function to be IT-computable, and hence it is indeed true for recursive programs. We also showed that  $h$  is ISIT-decidable but that  $H$  is equivalent to the halting problem for ISIT-machines, and therefore not ISIT-decidable.

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<sup>7</sup> $A$  is  $\underline{\Pi}_1^1$  if there is a second-order arithmetical formula  $\forall \bar{X} \psi(\bar{X}, Y, \bar{B})$ , where  $\bar{B}$  are set parameters and  $\psi$  is a first-order formula, that defines  $A$  in the standard model.

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